

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				2 *****
				3 *
				4 *            Zvector E6 instruction tests for VRR-b encoded:
				5 *
				6 *            E674 VSCHP    -   DECIMAL SCALE AND CONVERT TO HFP
				7 *
				8 *            James Wekel June 2024
				9 *****
				11 *****
				12 *
				13 *            basic instruction tests
				14 *
				15 *****
				16 *    This program tests proper functioning of the z/arch E6 VRR-b decimal
				17 *    scale and convert to HFP instruction. Exceptions are not tested.
				18 *
				19 *    PLEASE NOTE that the tests are very SIMPLE TESTS designed to catch
				20 *    obvious coding errors. None of the tests are thorough. They are
				21 *    NOT designed to test all aspects of any of the instructions.
				22 *
				23 *****
				24 *
				25 *    A cross-check test is performed if the rounding mode is zero,
				26 *    and the shifted packed decimal source can be converted to a 64-bit
				27 *    fixed value without overflow. The cross-check test converts the
				28 *    packed decimal source, uses CEGR, CDGR or CXGR to convert to
				29 *    HFP. This result is compared to VSCHP result. An XCHECK test
				30 *    error message will be issued if there is a difference.
				31 *
				32 *****
				33 *
				34 *    *Testcase zvector-e6-17-VSCHP: VECTOR E6 VRR-b VSCHP instruction
				35 *    *
				36 *    *            Zvector E6 instruction tests for VRR-b encoded:
				37 *    *
				38 *    *            E674 VSCHP    -   DECIMAL SCALE AND CONVERT TO HFP
				39 *    *
				40 *    *            # -----
				41 *    *            #    This tests only the basic function of the instruction.
				42 *    *            #    Exceptions are NOT tested.
				43 *    *            # -----
				44 *    *
				45 *    main size        2
				46 *    numcpu          1
				47 *    sysclear
				48 *    archlvl        z/Arch
				49 *    *
				50 *    loadcore        "\$(testpath)/zvector-e6-17-VSCHP.core" 0x0
				51 *    *
				52 *    diag8cmd        enable    # (needed for messages to Hercules console)
				53 *    runtest 2
				54 *    diag8cmd        disable   # (reset back to default)
				55 *    *
				56 *    *Done

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
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57 \*

58 \*\*\*\*\*

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				60 *****
				61 * FCHECK Macro - Is a Facility Bit set?
				62 *
				63 * If the facility bit is NOT set, an message is issued and
				64 * the test is skipped.
				65 *
				66 * Fcheck uses R0, R1 and R2
				67 *
				68 * eg. FCHECK 134, 'vector-packed-decimal'
				69 *****
				70 MACRO
				71 FCHECK &BITNO, &NOTSETMSG
				72 . * &BITNO : facility bit number to check
				73 . * &NOTSETMSG : 'facility name'
				74 LCLA &FBBYTE Facility bit in Byte
				75 LCLA &FBBIT Facility bit within Byte
				76
				77 LCLA &L(8)
				78 &L(1) SetA 128, 64, 32, 16, 8, 4, 2, 1 bit positions within byte
				79
				80 &FBBYTE SETA &BITNO/8
				81 &FBBIT SETA &L((&BITNO-(&FBBYTE*8))+1)
				82 . * MNOTE 0, 'checking Bit=&BITNO: FBBYTE=&FBBYTE, FBBIT=&FBBIT'
				83
				84 B X&SYSNDX
				85 * Fcheck data area
				86 * skip messgae
				87 SKT&SYSNDX DC C' Skipping tests: '
				88 DC C&NOTSETMSG
				89 DC C' facility (bit &BITNO) is not installed.'
				90 SKL&SYSNDX EQU *-SKT&SYSNDX
				91 * facility bits
				92 DS FD gap
				93 FB&SYSNDX DS 4FD
				94 DS FD gap
				95 *
				96 X&SYSNDX EQU *
				97 LA R0, ((X&SYSNDX- FB&SYSNDX)/8)-1
				98 STFLE FB&SYSNDX get facility bits
				99
				100 XGR R0, R0
				101 IC R0, FB&SYSNDX+&FBBYTE get fbit byte
				102 N R0, =F' &FBBIT' is bit set?
				103 BNZ XC&SYSNDX
				104 *
				105 * facility bit not set, issue message and exit
				106 *
				107 LA R0, SKL&SYSNDX message length
				108 LA R1, SKT&SYSNDX message address
				109 BAL R2, MSG
				110
				111 B EOJ
				112 XC&SYSNDX EQU *
				113 MEND



LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				134	
				135	*****
				136	* The actual "ZVE6TST" program itself...
				137	*****
				138	*
				139	* Architecture Mode: z/Arch
				140	* Register Usage:
				141	*
				142	* R0 (work)
				143	* R1-4 (work)
				144	* R5 Testing control table - current test base
				145	* R6- R7 (work)
				146	* R8 First base register
				147	* R9 Second base register
				148	* R10 Third base register
				149	* R11 E6TEST call return
				150	* R12 E6TESTS register
				151	* R13 (work)
				152	* R14 Subroutine call
				153	* R15 Secondary Subroutine call or work
				154	*
				155	*****
00000200		00000200		157	USING BEGIN, R8 FIRST Base Register
00000200		00001200		158	USING BEGIN+4096, R9 SECOND Base Register
00000200		00002200		159	USING BEGIN+8192, R10 THIRD Base Register
				160	
00000200	0580			161	BEGIN BALR R8, 0 Initalize FIRST base register
00000202	0680			162	BCTR R8, 0 Initalize FIRST base register
00000204	0680			163	BCTR R8, 0 Initalize FIRST base register
				164	
00000206	4190 8800		00000800	165	LA R9, 2048(, R8) Initalize SECOND base register
0000020A	4190 9800		00000800	166	LA R9, 2048(, R9) Initalize SECOND base register
				167	
0000020E	41A0 9800		00000800	168	LA R10, 2048(, R9) Initalize THIRD base register
00000212	41A0 A800		00000800	169	LA R10, 2048(, R10) Initalize THIRD base register
				170	
00000216	B600 8444		00000644	171	STCTL R0, R0, CTLR0 Store CR0 to enable AFP
0000021A	9604 8445		00000645	172	OI CTLR0+1, X' 04' Turn on AFP bit
0000021E	9602 8445		00000645	173	OI CTLR0+1, X' 02' Turn on Vector bit
00000222	B700 8444		00000644	174	LCTL R0, R0, CTLR0 Reload updated CR0
				175	
				176	*****
				177	* Is Vector-packed-decimal-enhancement facility 2 installed (bit 192)
				178	*****
				179	
00000226	47F0 80C8		000002C8	180	FCHECK 192, 'vector-packed-decimal-enhancement facility 2'
				181+	B X0001
				182+	*
				183+	*
0000022A	40404040 40404040			184+	SKT0001 DC C' Skipping tests: '
00000244	A58583A3 96996097			185+	DC C' vector-packed-decimal-enhancement facility 2'
00000270	40868183 899389A3			186+	DC C' facility (bit 192) is not installed.'
		0000006B 00000001		187+	SKL0001 EQU *-SKT0001
				188+	*
00000298	00000000 00000000			189+	DS FD facility bits gap





LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				240 *-----
				241 * For small (19 digit) values, cross check result
				242 * if rounding mode = 0 and conversion to 64-bit does not overflow
				243 *
				244 * R15 - RETURN
				245 *
				246 * v1,v2,v3 have result, source, scale
				247 *-----
		0000032E	00000001	248 XCHECK EQU *
				249
0000032E	B982 0011			250 XGR R1,R1 Is Rounding Mode = 0?
00000332	4310 5008		00000008	251 IC R1,M5 get M5
00000336	A517 0001			252 NILL r1,1 RM: bit 3
0000033A	1211			253 LTR R1,R1
0000033C	477F 0000		00000000	254 BNZ 0(R15) not zero RM ignore test
				255
00000340	E7B2 0000 0056			256 VLR V11,V2 copy source
00000346	E6AB 3019 F072			257 VSRPR V10,V11,V3,159,1 shift
0000034C	071F			258 BCR 1,R15 cc=3: overflow: ignore and return
				259
0000034E	E60A 0018 0052			260 VCVBG R0,V10,1,8 get 64-bit binary value
00000354	071F			261 BCR 1,15 cc=3: overflow: ignore and return
				262
00000356	E640 8454 2004		00000654	263 VLLEBRZ V4,=F'0',2 zero V4 (FPR4)
0000035C	E660 8454 2004		00000654	264 VLLEBRZ V6,=F'0',2 zero V6 (FPR6)
				265 *
				266 * convert R0 to appropriate HFP format
				267 * m4: 2-> short, 3->long, 4->extended
				268 *
00000362	B982 0011			269 XGR R1,R1
00000366	4310 5007		00000007	270 IC R1,M4 get hfp format
0000036A	5910 8458		00000658	271 C R1,=F'2'
0000036E	4780 8184		00000384	272 BE XCSHORT
00000372	5910 845C		0000065C	273 C R1,=F'3'
00000376	4780 8194		00000394	274 BE XCLONG
0000037A	5910 8460		00000660	275 C R1,=F'4'
0000037E	4780 81A4		000003A4	276 BE XCEXT
00000382	07FF			277 BR R15 invalid format: ignore
				278
				279 * hfp - short
00000384				280 XCSHORT DS 0F
00000384	B3C4 0040			281 CEGR FPR4,R0 convert r0 to short hfp
00000388	E740 8258 000E		00000458	282 VST V4,XCRESULT
0000038E	47F0 81B4		000003B4	283 B XC001
				284
				285 * hfp - long
00000394				286 XCLONG DS 0F
00000394	B3C5 0040			287 CDGR FPR4,R0 convert r0 to long hfp
00000398	E740 8258 000E		00000458	288 VST V4,XCRESULT
0000039E	47F0 81B4		000003B4	289 B XC001
				290
				291 * hfp - extended
000003A4				292 XCEXT DS 0F
000003A4	B3C6 0040			293 CXGR FPR4,R0 convert r0 to extended hfp: FPR4 & FPR6
000003A8	E740 8258 000A		00000458	294 VSTEG V4,XCRESULT,0 save high order extended hfp
000003AE	E760 8260 000A		00000460	295 VSTEG V6,XCRESULT+8,0 save low order extended hfp

LOC	OBJECT CODE			ADDR1	ADDR2	STMT			
						296			
				000003B4	00000001	297	XC001	EQU	*
000003B4	E710	8268	000E		00000468	298		VST	V1, XCV1
000003BA	D50F	8268	8258	00000468	00000458	299		CLC	XCV1, XCRESULT
000003C0	478F	0000			00000000	300		BE	0(R15)      0k, exit
						301			
						302	* xcheck failed message		
						303			
000003C4	4820	5004			00000004	304		LH	R2, TNUM      get test number and convert
000003C8	4E20	8EEB			000010EB	305		CVD	R2, DECNUM
000003CC	D211	8ED5	8EBF	000010D5	000010BF	306		MVC	PRT3, EDIT
000003D2	DE11	8ED5	8EEB	000010D5	000010EB	307		ED	PRT3, DECNUM
000003D8	D202	8E6D	8EE2	0000106D	000010E2	308		MVC	XCPTNUM(3), PRT3+13      fill in message with test #
						309			
000003DE	D207	8E8F	5010	0000108F	00000010	310		MVC	XCPNAME, OPNAME      fill in message with instruction
						311			
000003E4	B982	0022				312		XGR	R2, R2      get m4 as U8
000003E8	4320	5007			00000007	313		IC	R2, M4
000003EC	4E20	8EEB			000010EB	314		CVD	R2, DECNUM      and convert
000003F0	D211	8ED5	8EBF	000010D5	000010BF	315		MVC	PRT3, EDIT
000003F6	DE11	8ED5	8EEB	000010D5	000010EB	316		ED	PRT3, DECNUM
000003FC	D201	8EA0	8EE3	000010A0	000010E3	317		MVC	XCPM4(2), PRT3+14      fill in message with m4 field
						318			
00000402	B982	0022				319		XGR	R2, R2      get m5 as U8
00000406	4320	5008			00000008	320		IC	R2, M5
0000040A	4E20	8EEB			000010EB	321		CVD	R2, DECNUM      and convert
0000040E	D211	8ED5	8EBF	000010D5	000010BF	322		MVC	PRT3, EDIT
00000414	DE11	8ED5	8EEB	000010D5	000010EB	323		ED	PRT3, DECNUM
0000041A	D201	8EAC	8EE3	000010AC	000010E3	324		MVC	XCPM5(2), PRT3+14      fill in message with m5 field
						325			
00000420	B982	0022				326		XGR	R2, R2      get scale as U8
00000424	4320	5009			00000009	327		IC	R2, SCALE      and convert
00000428	4E20	8EEB			000010EB	328		CVD	R2, DECNUM
0000042C	D211	8ED5	8EBF	000010D5	000010BF	329		MVC	PRT3, EDIT
00000432	DE11	8ED5	8EEB	000010D5	000010EB	330		ED	PRT3, DECNUM
00000438	D202	8EBB	8EE2	000010BB	000010E2	331		MVC	XCPSCALE(3), PRT3+13      fill in message with scale field
						332			
0000043E	50F0	8278			00000478	333		ST	R15, XCR15      save r15
00000442	4100	005F			0000005F	334		LA	R0, XCPLNG      message length
00000446	4110	8E60			00001060	335		LA	R1, XCPLINE      messagfe address
0000044A	45F0	8324			00000524	336		BAL	R15, RPTERROR
						337			
0000044E	58F0	8278			00000478	338		L	R15, XCR15
00000452	07FF					339		BR	R15      return from xcheck
						340			
00000458						341		DS	0FD
00000458	00000000	00000000				342	XCRESULT	DS	XL16
00000468	00000000	00000000				343	XCV1	ds	XL16
00000478	00000000	00000000				344	XCR15	DS	FD
						345			

LOC	OBJECT CODE			ADDR1	ADDR2	STMT	
						347	*****
						348	* result not as expected:
						349	* issue message with test number, instruction under test
						350	* and instruction m4, m5
						351	*****
				00000480	00000001	352	FAILMSG EQU *
00000480	4820	5004			00000004	353	LH R2, TNUM get test number and convert
00000484	4E20	8EEB			000010EB	354	CVD R2, DECNUM
00000488	D211	8ED5 8EBF		000010D5	000010BF	355	MVC PRT3, EDIT
0000048E	DE11	8ED5 8EEB		000010D5	000010EB	356	ED PRT3, DECNUM
00000494	D202	8E15 8EE2		00001015	000010E2	357	MVC PRTNUM(3), PRT3+13 fill in message with test #
						358	
0000049A	D207	8E30 5010		00001030	00000010	359	MVC PRTNAME, OPNAME fill in message with instruction
						360	*
000004A0	B982	0022				361	XGR R2, R2
000004A4	4320	5007			00000007	362	IC R2, M4 get m4 and convert
000004A8	4E20	8EEB			000010EB	363	CVD R2, DECNUM
000004AC	D211	8ED5 8EBF		000010D5	000010BF	364	MVC PRT3, EDIT
000004B2	DE11	8ED5 8EEB		000010D5	000010EB	365	ED PRT3, DECNUM
000004B8	D201	8E41 8EE3		00001041	000010E3	366	MVC PRTM4(2), PRT3+14 fill in message with m4 field
						367	*
000004BE	B982	0022				368	XGR R2, R2
000004C2	4320	5008			00000008	369	IC R2, M5 get m5 and convert
000004C6	4E20	8EEB			000010EB	370	CVD R2, DECNUM
000004CA	D211	8ED5 8EBF		000010D5	000010BF	371	MVC PRT3, EDIT
000004D0	DE11	8ED5 8EEB		000010D5	000010EB	372	ED PRT3, DECNUM
000004D6	D201	8E4D 8EE3		0000104D	000010E3	373	MVC PRTM5(2), PRT3+14 fill in message with m5 field
						374	*
000004DC	B982	0022				375	XGR R2, R2
000004E0	4320	5009			00000009	376	IC R2, SCALE get scale and convert
000004E4	4E20	8EEB			000010EB	377	CVD R2, DECNUM
000004E8	D211	8ED5 8EBF		000010D5	000010BF	378	MVC PRT3, EDIT
000004EE	DE11	8ED5 8EEB		000010D5	000010EB	379	ED PRT3, DECNUM
000004F4	D202	8E5C 8EE2		0000105C	000010E2	380	MVC PRTSCALE(3), PRT3+13 fill in message with scale
						381	
000004FA	4100	0058			00000058	382	LA R0, PRTLNG message length
000004FE	4110	8E08			00001008	383	LA R1, PRTLINE messagfe address
00000502	45F0	8324			00000524	384	BAL R15, RPTERROR
						386	*****
						387	* continue after a failed test
						388	*****
				00000506	00000001	389	FAILCONT EQU *
00000506	5800	8464			00000664	390	L R0, =F' 1' set failed test indicator
0000050A	5000	8E00			00001000	391	ST R0, FAILED
						392	
0000050E	41C0	C004			00000004	393	LA R12, 4(0, R12) next test address
00000512	47F0	80F4			000002F4	394	B NEXTE6
						396	*****
						397	* end of testing; set ending psw
						398	*****
				00000516	00000001	399	ENDTEST EQU *
00000516	5810	8E00			00001000	400	L R1, FAILED did a test fail?







LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				469	*****
				470	*            Normal completion or Abnormal termination PSWs
				471	*****
00000618	00020001 80000000			473	E0JPSW    DC        0D' 0' , X' 0002000180000000' , AD(0)
00000628	B2B2 8418		00000618	475	E0J            LPSWE E0JPSW                    Normal completion
00000630	00020001 80000000			477	FAILPSW    DC        0D' 0' , X' 0002000180000000' , AD(X' BAD' )
00000640	B2B2 8430		00000630	479	FAILTEST    LPSWE FAILPSW                    Abnormal termination
				481	*****
				482	*            Working Storage
				483	*****
00000644	00000000			485	CTLRO        DS        F                    CRO
00000648	00000000			486	DS        F
0000064C				488	
0000064C	00000080			489	LTORG ,                    Literals pool
00000650	00003868			490	=F' 128'
00000654	00000000			491	=A(E6TESTS)
00000658	00000002			492	=F' 0'
0000065C	00000003			493	=F' 2'
00000660	00000004			494	=F' 3'
00000664	00000001			495	=F' 4'
00000668	0000			496	=F' 1'
0000066A	005F			497	=H' 0'
				498	=AL2(L' MSGMSG)
				499	
				500	*            some constants
				501	
	00000400	00000001		502	K            EQU        1024                    One KB
	00001000	00000001		503	PAGE        EQU        (4*K)                    Size of one page
	00010000	00000001		504	K64        EQU        (64*K)                    64 KB
	00100000	00000001		505	MB        EQU        (K*K)                    1 MB
				506	
	AABBCCDD	00000001		507	REG2PATT    EQU        X' AABBCCDD'            Polluted Register pattern
	000000DD	00000001		508	REG2LOW    EQU                    X' DD'            (last byte above)

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				510 *=====
				511 *
				512 * NOTE: start data on an address that is easy to display
				513 * within Hercules
				514 *
				515 *=====
0000066C		0000066C	00001000	516
00001000	00000000			517 ORG ZVE6TST+X' 1000'
00001004	00000000			518 FAILED DC F' 0' some test failed?
				519 TESTING DC F' 0' current test #
				521 *****
				522 * TEST failed : result messgae
				523 *****
				524 *
				525 * failed message and associated editing
				526 *
00001008	40404040	4040E385		527 PRTLIN DC C' Test # '
00001015	A7A7A7			528 PRTNUM DC C' xxx'
00001018	40868189	93858440		529 DC c' failed for instruction '
00001030	A7A7A7A7	A7A7A7A7		530 PRTNAME DC CL8' xxxxxxxx'
00001038	40A689A3	884094F4		531 DC C' with m4=
00001041	A7A7			532 PRTM4 DC C' xx'
00001043	6B40A689	A3884094		533 DC C', with m5=
0000104D	A7A7			534 PRTM5 DC C' xx'
0000104F	6B40A689	A38840A2		535 DC C', with scale=
0000105C	A7A7A7			536 PRTSCALE DC C' xxx'
0000105F	4B			537 DC C'.'
		00000058	00000001	538 PRTLNG EQU *- PRTLIN
				540 *****
				541 * TEST failed : XCHECK
				542 *****
				543 *
				544 * XCHECK failed message
				545 *
00001060	40404040	4040E385		546 XCPLINE DC C' Test # '
0000106D	A7A7A7			547 XCPTNUM DC C' xxx'
00001070	40E7C3C8	C5C3D240		548 DC c' XCHECK failed for instruction '
0000108F	A7A7A7A7	A7A7A7A7		549 XCPNAME DC CL8' xxxxxxxx'
00001097	40A689A3	884094F4		550 DC C' with m4=
000010A0	A7A7			551 XCPM4 DC C' xx'
000010A2	6B40A689	A3884094		552 DC C', with m5=
000010AC	A7A7			553 XCPM5 DC C' xx'
000010AE	6B40A689	A38840A2		554 DC C', with scale=
000010BB	A7A7A7			555 XCPSCALE DC C' xxx'
000010BE	4B			556 DC C'.'
		0000005F	00000001	557 XCPLNG EQU *- XCPLINE



LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				580 *****
				581 * E6TEST DSECT
				582 *****
				584 E6TEST DSECT ,
00000000	00000000			585 TSUB DC A(0) pointer to test
00000004	0000			586 TNUM DC H' 00' Test Number
00000006	00			587 DC X' 00'
00000007	00			588 M4 DC HL1' 00' m4 used
00000008	00			589 M5 DC HL1' 00' m5 used
00000009	00			590 SCALE DC HL1' 00' scale used
0000000C	00000000			591 V2ADDR DC A(0) address of v2: 16-byte packed decimal
00000010	40404040 40404040			592 OPNAME DC CL8' ' E6 name
00000018	00000000			593 RELEN DC A(0) result length
0000001C	00000000			594 READDR DC A(0) expected result address
				595
				596 **
				597 * test routine will be here (from VRR-b macro)
0000115C		00000000	00003A17	599 ZVE6TST CSECT ,
				600 DS 0F
				602 *****
				603 * Macros to help build test tables
				604 *****
				606 *
				607 * macro to generate individual test
				608 *
				609 MACRO
				610 VRR_B &INST, &M4, &M5, &SCALE
				611 . * &INST - VRR-b instruction under test
				612 . * &m4 - m4 field
				613 . * &m5 - m5 field
				614 GBLA &TNUM
				615 &TNUM SETA &TNUM+1
				616
				617 DS 0FD
				618 USING *, R5 base for test data and test routine
				619
				620 T&TNUM DC A(X&TNUM) address of test routine
				621 DC H' &TNUM test number
				622 DC X' 00'
				623 DC HL1' &M4' m4
				624 DC HL1' &M5' m5
				625 V3_&TNUM DC HL1' &SCALE' scale
				626 V2_&TNUM DC A(RE&TNUM+16) address of v2: 16-byte packed decimal
				627 DC CL8' &INST' instruction name
				628 DC A(16) result length
				629 DC A(RE&TNUM) address of expected result
				630 . *



LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				671 *****
				672 * E6 VRR-b tests
				673 *****
				674 PRINT DATA
				675 *
				676 * E674 VSCHP - DECIMAL SCALE AND CONVERT TO HFP
				677 *
				678 *-----
				679 * VSCHP - DECIMAL SCALE AND CONVERT TO HFP
				680 *-----
				681 * VRR-b instruction, m4, m5, scale(0-8)
				682 * followed by
				683 * followed by
				684 * v1 - 16 byte expected result
				685 * v2 - 16 byte zoned decimal (operand)
				686 *-----
				687 * NO Shift - No Rounding
				688 *-----
				689 *-----
				690 * short float
				691 *-----
				692 * +0
				693 VRR_B VSCHP, 2, 0, 0
00001160				694+ DS OFD
00001160		00001160		695+ USING *, R5
00001160	00001180			696+T1 DC A(X1)
00001164	0001			697+ DC H' 1'
00001166	00			698+ DC X' 00'
00001167	02			699+ DC HL1' 2'
00001168	00			700+ DC HL1' 0'
00001169	00			701+V3_1 DC HL1' 0'
0000116C	000011B0			702+V2_1 DC A(RE1+16)
00001170	E5E2C3C8 D7404040			703+ DC CL8' VSCHP'
00001178	00000010			704+ DC A(16)
0000117C	000011A0			705+ DC A(RE1)
				706+*
00001180				707+X1 DS 0F
00001180	E710 8F2C 0006	0000112C		708+ VL V1, V1FUDGE
00001186	E320 500C 0014	0000116C		709+ LGF R2, V2_1
0000118C	E722 0000 0006	00000000		710+ VL V2, 0(R2)
00001192	E730 5009 7000	00001169		711+ VLEB V3, V3_1, 7
00001198	E612 3000 2074			712+ VSCHP V1, V2, V3, 2, 0
0000119E	07FB			713+ BR R11
000011A0				714+RE1 DS 0F
000011A0				715+ DROP R5
000011A0	00000000 00000000			716 DC XL16' 00000000000000000000000000000000'
000011A8	00000000 00000000			
000011B0	00000000 00000000			717 DC XL16' 00000000000000000000000000000000C'
000011B8	00000000 00000000C			
				718 * - 0
				719 VRR_B VSCHP, 2, 0, 0
000011C0				720+ DS OFD
000011C0		000011C0		721+ USING *, R5
000011C0	000011E0			722+T2 DC A(X2)
000011C4	0002			723+ DC H' 2'
000011C6	00			724+ DC X' 00'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000011C7	02			725+	DC	HL1' 2'	m4
000011C8	00			726+	DC	HL1' 0'	m5
000011C9	00			727+V3_2	DC	HL1' 0'	scale
000011CC	00001210			728+V2_2	DC	A(RE2+16)	address of v2: 16-byte packed decimal
000011D0	E5E2C3C8 D7404040			729+	DC	CL8' VSCHP'	instruction name
000011D8	00000010			730+	DC	A(16)	result length
000011DC	00001200			731+	DC	A(RE2)	address of expected result
				732+*			
000011E0				733+X2	DS	0F	
000011E0	E710 8F2C 0006		0000112C	734+	VL	V1, V1FUDGE	fudge V1
000011E6	E320 500C 0014		000011CC	735+	LGF	R2, V2_2	get v2
000011EC	E722 0000 0006		00000000	736+	VL	V2, 0(R2)	
000011F2	E730 5009 7000		000011C9	737+	VLEB	V3, V3_2, 7	get v3 scale
000011F8	E612 3000 2074			738+	VSCHP	V1, V2, V3, 2, 0	test instruction
000011FE	07FB			739+	BR	R11	return
00001200				740+RE2	DS	0F	expected 16 byte result
00001200				741+	DROP	R5	
00001200	00000000 00000000			742	DC	XL16' 00000000000000000000000000000000'	
00001208	00000000 00000000						
00001210	00000000 00000000			743	DC	XL16' 0000000000000000000000000000000D'	
00001218	00000000 0000000D						
				744 * +1			
				745	VRR_B	VSCHP, 2, 0, 0	
00001220				746+	DS	0FD	
00001220		00001220		747+	USING	*, R5	base for test data and test routine
00001220	00001240			748+T3	DC	A(X3)	address of test routine
00001224	0003			749+	DC	H' 3'	test number
00001226	00			750+	DC	X' 00'	
00001227	02			751+	DC	HL1' 2'	m4
00001228	00			752+	DC	HL1' 0'	m5
00001229	00			753+V3_3	DC	HL1' 0'	scale
0000122C	00001270			754+V2_3	DC	A(RE3+16)	address of v2: 16-byte packed decimal
00001230	E5E2C3C8 D7404040			755+	DC	CL8' VSCHP'	instruction name
00001238	00000010			756+	DC	A(16)	result length
0000123C	00001260			757+	DC	A(RE3)	address of expected result
				758+*			
00001240				759+X3	DS	0F	
00001240	E710 8F2C 0006		0000112C	760+	VL	V1, V1FUDGE	fudge V1
00001246	E320 500C 0014		0000122C	761+	LGF	R2, V2_3	get v2
0000124C	E722 0000 0006		00000000	762+	VL	V2, 0(R2)	
00001252	E730 5009 7000		00001229	763+	VLEB	V3, V3_3, 7	get v3 scale
00001258	E612 3000 2074			764+	VSCHP	V1, V2, V3, 2, 0	test instruction
0000125E	07FB			765+	BR	R11	return
00001260				766+RE3	DS	0F	expected 16 byte result
00001260				767+	DROP	R5	
00001260	41100000 00000000			768	DC	XL16' 41100000000000000000000000000000'	
00001268	00000000 00000000						
00001270	00000000 00000000			769	DC	XL16' 00000000000000000000000000000001C'	
00001278	00000000 0000001C						
				770 * -1			
				771	VRR_B	VSCHP, 2, 0, 0	
00001280				772+	DS	0FD	
00001280		00001280		773+	USING	*, R5	base for test data and test routine
00001280	000012A0			774+T4	DC	A(X4)	address of test routine
00001284	0004			775+	DC	H' 4'	test number
00001286	00			776+	DC	X' 00'	



LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00001344	0006			829+	DC	H' 6' test number
00001346	00			830+	DC	X' 00'
00001347	02			831+	DC	HL1' 2' m4
00001348	00			832+	DC	HL1' 0' m5
00001349	00			833+V3_6	DC	HL1' 0' scale
0000134C	00001390			834+V2_6	DC	A(RE6+16) address of v2: 16-byte packed decimal
00001350	E5E2C3C8 D7404040			835+	DC	CL8' VSCHP' instruction name
00001358	00000010			836+	DC	A(16) result length
0000135C	00001380			837+	DC	A(RE6) address of expected result
				838+*		
00001360				839+X6	DS	0F
00001360	E710 8F2C 0006		0000112C	840+	VL	V1, V1FUDGE fudge V1
00001366	E320 500C 0014		0000134C	841+	LGF	R2, V2_6 get v2
0000136C	E722 0000 0006		00000000	842+	VL	V2, 0(R2)
00001372	E730 5009 7000		00001349	843+	VLEB	V3, V3_6, 7 get v3 scale
00001378	E612 3000 2074			844+	VSCHP	V1, V2, V3, 2, 0 test instruction
0000137E	07FB			845+	BR	R11 return
00001380				846+RE6	DS	0F expected 16 byte result
00001380				847+	DROP	R5
00001380	D0800000 00000000			848	DC	XL16' D0800000000000000000000000000000'
00001388	00000000 00000000					
00001390	00000000 00009223			849	DC	XL16' 00000000000009223372036854775808D'
00001398	37203685 4775808D					
				850		
				851 *		9223372036854775807
				852	VRR_B	VSCHP, 2, 0, 0
000013A0				853+	DS	0FD
000013A0		000013A0		854+	USING	*, R5 base for test data and test routine
000013A0	000013C0			855+T7	DC	A(X7) address of test routine
000013A4	0007			856+	DC	H' 7' test number
000013A6	00			857+	DC	X' 00'
000013A7	02			858+	DC	HL1' 2' m4
000013A8	00			859+	DC	HL1' 0' m5
000013A9	00			860+V3_7	DC	HL1' 0' scale
000013AC	000013F0			861+V2_7	DC	A(RE7+16) address of v2: 16-byte packed decimal
000013B0	E5E2C3C8 D7404040			862+	DC	CL8' VSCHP' instruction name
000013B8	00000010			863+	DC	A(16) result length
000013BC	000013E0			864+	DC	A(RE7) address of expected result
				865+*		
000013C0				866+X7	DS	0F
000013C0	E710 8F2C 0006		0000112C	867+	VL	V1, V1FUDGE fudge V1
000013C6	E320 500C 0014		000013AC	868+	LGF	R2, V2_7 get v2
000013CC	E722 0000 0006		00000000	869+	VL	V2, 0(R2)
000013D2	E730 5009 7000		000013A9	870+	VLEB	V3, V3_7, 7 get v3 scale
000013D8	E612 3000 2074			871+	VSCHP	V1, V2, V3, 2, 0 test instruction
000013DE	07FB			872+	BR	R11 return
000013E0				873+RE7	DS	0F expected 16 byte result
000013E0				874+	DROP	R5
000013E0	507FFFFFFF 00000000			875	DC	XL16' 507FFFFFFF000000000000000000000000'
000013E8	00000000 00000000					
000013F0	00000000 00009223			876	DC	XL16' 00000000000009223372036854775807C'
000013F8	37203685 4775807C					
				877		
				878 *		18446744073709551615
				879	VRR_B	VSCHP, 2, 0, 0
00001400				880+	DS	0FD







LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00001630	00000000 00000009			1038	DC	XL16' 00000000000000009000000000000001C'
00001638	00000000 0000001C					
				1039		
				1040 *	- 9223372036854775808	
				1041	VRR_B VSCHP, 3, 0, 0	
00001640				1042+	DS	OFD
00001640		00001640		1043+	USING	*, R5
00001640	00001660			1044+T14	DC	A(X14)
00001644	000E			1045+	DC	H' 14'
00001646	00			1046+	DC	X' 00'
00001647	03			1047+	DC	HL1' 3'
00001648	00			1048+	DC	HL1' 0'
00001649	00			1049+V3_14	DC	HL1' 0'
0000164C	00001690			1050+V2_14	DC	A(RE14+16)
00001650	E5E2C3C8 D7404040			1051+	DC	CL8' VSCHP'
00001658	00000010			1052+	DC	A(16)
0000165C	00001680			1053+	DC	A(RE14)
				1054+*		
00001660				1055+X14	DS	OF
00001660	E710 8F2C 0006		0000112C	1056+	VL	V1, V1FUDGE
00001666	E320 500C 0014		0000164C	1057+	LGF	R2, V2_14
0000166C	E722 0000 0006		00000000	1058+	VL	V2, 0(R2)
00001672	E730 5009 7000		00001649	1059+	VLEB	V3, V3_14, 7
00001678	E612 3000 3074			1060+	VSCHP	V1, V2, V3, 3, 0
0000167E	07FB			1061+	BR	R11
00001680				1062+RE14	DS	OF
00001680				1063+	DROP	R5
00001680	D0800000 00000000			1064	DC	XL16' D0800000000000000000000000000000'
00001688	00000000 00000000					
00001690	00000000 00009223			1065	DC	XL16' 00000000000009223372036854775808D'
00001698	37203685 4775808D					
				1066		
				1067 *	9223372036854775807	
				1068	VRR_B VSCHP, 3, 0, 0	
000016A0				1069+	DS	OFD
000016A0		000016A0		1070+	USING	*, R5
000016A0	000016C0			1071+T15	DC	A(X15)
000016A4	000F			1072+	DC	H' 15'
000016A6	00			1073+	DC	X' 00'
000016A7	03			1074+	DC	HL1' 3'
000016A8	00			1075+	DC	HL1' 0'
000016A9	00			1076+V3_15	DC	HL1' 0'
000016AC	000016F0			1077+V2_15	DC	A(RE15+16)
000016B0	E5E2C3C8 D7404040			1078+	DC	CL8' VSCHP'
000016B8	00000010			1079+	DC	A(16)
000016BC	000016E0			1080+	DC	A(RE15)
				1081+*		
000016C0				1082+X15	DS	OF
000016C0	E710 8F2C 0006		0000112C	1083+	VL	V1, V1FUDGE
000016C6	E320 500C 0014		000016AC	1084+	LGF	R2, V2_15
000016CC	E722 0000 0006		00000000	1085+	VL	V2, 0(R2)
000016D2	E730 5009 7000		000016A9	1086+	VLEB	V3, V3_15, 7
000016D8	E612 3000 3074			1087+	VSCHP	V1, V2, V3, 3, 0
000016DE	07FB			1088+	BR	R11
000016E0				1089+RE15	DS	OF
000016E0				1090+	DROP	R5

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
000016E0	507FFFFFFF FFFFFFFF			1091	DC	XL16' 507FFFFFFFFFFFFFFFFF0000000000000000'
000016E8	00000000 00000000					
000016F0	00000000 00009223			1092	DC	XL16' 00000000000009223372036854775807C'
000016F8	37203685 4775807C					
				1093		
				1094 *	18446744073709551615	
				1095	VRR_B VSCHP, 3, 0, 0	
00001700				1096+	DS	OFD
00001700		00001700		1097+	USING	*, R5
00001700	00001720			1098+T16	DC	A(X16)
00001704	0010			1099+	DC	H' 16'
00001706	00			1100+	DC	X' 00'
00001707	03			1101+	DC	HL1' 3'
00001708	00			1102+	DC	HL1' 0'
00001709	00			1103+V3_16	DC	HL1' 0'
0000170C	00001750			1104+V2_16	DC	A(RE16+16)
00001710	E5E2C3C8 D7404040			1105+	DC	CL8' VSCHP'
00001718	00000010			1106+	DC	A(16)
0000171C	00001740			1107+	DC	A(RE16)
				1108+*		
00001720				1109+X16	DS	OF
00001720	E710 8F2C 0006		0000112C	1110+	VL	V1, V1FUDGE
00001726	E320 500C 0014		0000170C	1111+	LGF	R2, V2_16
0000172C	E722 0000 0006		00000000	1112+	VL	V2, 0(R2)
00001732	E730 5009 7000		00001709	1113+	VLEB	V3, V3_16, 7
00001738	E612 3000 3074			1114+	VSCHP	V1, V2, V3, 3, 0
0000173E	07FB			1115+	BR	R11
00001740				1116+RE16	DS	OF
00001740				1117+	DROP	R5
00001740	50FFFFFF FFFFFFFF			1118	DC	XL16' 50FFFFFFFFFFFFFFFFF000000000000000'
00001748	00000000 00000000					
00001750	00000000 00018446			1119	DC	XL16' 00000000000018446744073709551615C'
00001758	74407370 9551615C					
				1120		
				1121 *	-----	
				1122 *	extended float	
				1123 *	-----	
				1124 *	+0	
				1125	VRR_B VSCHP, 4, 0, 0	
00001760				1126+	DS	OFD
00001760		00001760		1127+	USING	*, R5
00001760	00001780			1128+T17	DC	A(X17)
00001764	0011			1129+	DC	H' 17'
00001766	00			1130+	DC	X' 00'
00001767	04			1131+	DC	HL1' 4'
00001768	00			1132+	DC	HL1' 0'
00001769	00			1133+V3_17	DC	HL1' 0'
0000176C	000017B0			1134+V2_17	DC	A(RE17+16)
00001770	E5E2C3C8 D7404040			1135+	DC	CL8' VSCHP'
00001778	00000010			1136+	DC	A(16)
0000177C	000017A0			1137+	DC	A(RE17)
				1138+*		
00001780				1139+X17	DS	OF
00001780	E710 8F2C 0006		0000112C	1140+	VL	V1, V1FUDGE
00001786	E320 500C 0014		0000176C	1141+	LGF	R2, V2_17
0000178C	E722 0000 0006		00000000	1142+	VL	V2, 0(R2)

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001792	E730 5009 7000		00001769	1143+	VLEB	V3, V3_17, 7	get v3 scale
00001798	E612 3000 4074			1144+	VSCHP	V1, V2, V3, 4, 0	test instruction
0000179E	07FB			1145+	BR	R11	return
000017A0				1146+RE17	DS	0F	expected 16 byte result
000017A0				1147+	DROP	R5	
000017A0	00000000 00000000			1148	DC	XL16' 00000000000000000000000000000000'	
000017A8	00000000 00000000						
000017B0	00000000 00000000			1149	DC	XL16' 00000000000000000000000000000000C'	
000017B8	00000000 0000000C						
				1150 * -0			
				1151	VRR_B	VSCHP, 4, 0, 0	
000017C0				1152+	DS	0FD	
000017C0		000017C0		1153+	USING	*, R5	base for test data and test routine
000017C0	000017E0			1154+T18	DC	A(X18)	address of test routine
000017C4	0012			1155+	DC	H' 18'	test number
000017C6	00			1156+	DC	X' 00'	
000017C7	04			1157+	DC	HL1' 4'	m4
000017C8	00			1158+	DC	HL1' 0'	m5
000017C9	00			1159+V3_18	DC	HL1' 0'	scale
000017CC	00001810			1160+V2_18	DC	A(RE18+16)	address of v2: 16-byte packed decimal
000017D0	E5E2C3C8 D7404040			1161+	DC	CL8' VSCHP'	instruction name
000017D8	00000010			1162+	DC	A(16)	result length
000017DC	00001800			1163+	DC	A(RE18)	address of expected result
				1164+*			
000017E0				1165+X18	DS	0F	
000017E0	E710 8F2C 0006		0000112C	1166+	VL	V1, V1FUDGE	fudge V1
000017E6	E320 500C 0014		000017CC	1167+	LGF	R2, V2_18	get v2
000017EC	E722 0000 0006		00000000	1168+	VL	V2, 0(R2)	
000017F2	E730 5009 7000		000017C9	1169+	VLEB	V3, V3_18, 7	get v3 scale
000017F8	E612 3000 4074			1170+	VSCHP	V1, V2, V3, 4, 0	test instruction
000017FE	07FB			1171+	BR	R11	return
00001800				1172+RE18	DS	0F	expected 16 byte result
00001800				1173+	DROP	R5	
00001800	00000000 00000000			1174	DC	XL16' 00000000000000000000000000000000'	
00001808	00000000 00000000						
00001810	00000000 00000000			1175	DC	XL16' 00000000000000000000000000000000D'	
00001818	00000000 0000000D						
				1176 * +1			
				1177	VRR_B	VSCHP, 4, 0, 0	
00001820				1178+	DS	0FD	
00001820		00001820		1179+	USING	*, R5	base for test data and test routine
00001820	00001840			1180+T19	DC	A(X19)	address of test routine
00001824	0013			1181+	DC	H' 19'	test number
00001826	00			1182+	DC	X' 00'	
00001827	04			1183+	DC	HL1' 4'	m4
00001828	00			1184+	DC	HL1' 0'	m5
00001829	00			1185+V3_19	DC	HL1' 0'	scale
0000182C	00001870			1186+V2_19	DC	A(RE19+16)	address of v2: 16-byte packed decimal
00001830	E5E2C3C8 D7404040			1187+	DC	CL8' VSCHP'	instruction name
00001838	00000010			1188+	DC	A(16)	result length
0000183C	00001860			1189+	DC	A(RE19)	address of expected result
				1190+*			
00001840				1191+X19	DS	0F	
00001840	E710 8F2C 0006		0000112C	1192+	VL	V1, V1FUDGE	fudge V1
00001846	E320 500C 0014		0000182C	1193+	LGF	R2, V2_19	get v2
0000184C	E722 0000 0006		00000000	1194+	VL	V2, 0(R2)	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001852	E730 5009 7000		00001829	1195+	VLEB	V3, V3_19, 7	get v3 scale
00001858	E612 3000 4074			1196+	VSCHP	V1, V2, V3, 4, 0	test instruction
0000185E	07FB			1197+	BR	R11	return
00001860				1198+RE19	DS	0F	expected 16 byte result
00001860				1199+	DROP	R5	
00001860	41100000 00000000			1200	DC	XL16' 4110000000000000003300000000000000'	
00001868	33000000 00000000						
00001870	00000000 00000000			1201	DC	XL16' 000000000000000000000000000000001C'	
00001878	00000000 0000001C						
				1202 * -1			
				1203	VRR_B	VSCHP, 4, 0, 0	
00001880				1204+	DS	0FD	
00001880		00001880		1205+	USING	*, R5	base for test data and test routine
00001880	000018A0			1206+T20	DC	A(X20)	address of test routine
00001884	0014			1207+	DC	H' 20'	test number
00001886	00			1208+	DC	X' 00'	
00001887	04			1209+	DC	HL1' 4'	m4
00001888	00			1210+	DC	HL1' 0'	m5
00001889	00			1211+V3_20	DC	HL1' 0'	scale
0000188C	000018D0			1212+V2_20	DC	A(RE20+16)	address of v2: 16-byte packed decimal
00001890	E5E2C3C8 D7404040			1213+	DC	CL8' VSCHP'	instruction name
00001898	00000010			1214+	DC	A(16)	result length
0000189C	000018C0			1215+	DC	A(RE20)	address of expected result
				1216+*			
000018A0				1217+X20	DS	0F	
000018A0	E710 8F2C 0006		0000112C	1218+	VL	V1, V1FUDGE	fudge V1
000018A6	E320 500C 0014		0000188C	1219+	LGF	R2, V2_20	get v2
000018AC	E722 0000 0006		00000000	1220+	VL	V2, 0(R2)	
000018B2	E730 5009 7000		00001889	1221+	VLEB	V3, V3_20, 7	get v3 scale
000018B8	E612 3000 4074			1222+	VSCHP	V1, V2, V3, 4, 0	test instruction
000018BE	07FB			1223+	BR	R11	return
000018C0				1224+RE20	DS	0F	expected 16 byte result
000018C0				1225+	DROP	R5	
000018C0	C1100000 00000000			1226	DC	XL16' C11000000000000000B3000000000000000'	
000018C8	B3000000 00000000						
000018D0	00000000 00000000			1227	DC	XL16' 000000000000000000000000000000001D'	
000018D8	00000000 0000001D						
				1228			
				1229 * +900000000000000001			
				1230	VRR_B	VSCHP, 4, 0, 0	
000018E0				1231+	DS	0FD	
000018E0		000018E0		1232+	USING	*, R5	base for test data and test routine
000018E0	00001900			1233+T21	DC	A(X21)	address of test routine
000018E4	0015			1234+	DC	H' 21'	test number
000018E6	00			1235+	DC	X' 00'	
000018E7	04			1236+	DC	HL1' 4'	m4
000018E8	00			1237+	DC	HL1' 0'	m5
000018E9	00			1238+V3_21	DC	HL1' 0'	scale
000018EC	00001930			1239+V2_21	DC	A(RE21+16)	address of v2: 16-byte packed decimal
000018F0	E5E2C3C8 D7404040			1240+	DC	CL8' VSCHP'	instruction name
000018F8	00000010			1241+	DC	A(16)	result length
000018FC	00001920			1242+	DC	A(RE21)	address of expected result
				1243+*			
00001900				1244+X21	DS	0F	
00001900	E710 8F2C 0006		0000112C	1245+	VL	V1, V1FUDGE	fudge V1
00001906	E320 500C 0014		000018EC	1246+	LGF	R2, V2_21	get v2

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0000190C	E722 0000 0006		00000000	1247+	VL	V2, 0(R2)	
00001912	E730 5009 7000		000018E9	1248+	VLEB	V3, V3_21, 7	get v3 scale
00001918	E612 3000 4074			1249+	VSCHP	V1, V2, V3, 4, 0	test instruction
0000191E	07FB			1250+	BR	R11	return
00001920				1251+RE21	DS	0F	expected 16 byte result
00001920				1252+	DROP	R5	
00001920	4E1FF973 CAFA8001			1253	DC	XL16' 4E1FF973CAFA80014000000000000000'	
00001928	40000000 00000000						
00001930	00000000 00000009			1254	DC	XL16' 00000000000000009000000000000001C'	
00001938	00000000 0000001C						
				1255			
				1256 *	- 9223372036854775808		
				1257	VRR_B	VSCHP, 4, 0, 0	
00001940				1258+	DS	0FD	
00001940		00001940		1259+	USING	*, R5	base for test data and test routine
00001940	00001960			1260+T22	DC	A(X22)	address of test routine
00001944	0016			1261+	DC	H' 22'	test number
00001946	00			1262+	DC	X' 00'	
00001947	04			1263+	DC	HL1' 4'	m4
00001948	00			1264+	DC	HL1' 0'	m5
00001949	00			1265+V3_22	DC	HL1' 0'	scale
0000194C	00001990			1266+V2_22	DC	A(RE22+16)	address of v2: 16-byte packed decimal
00001950	E5E2C3C8 D7404040			1267+	DC	CL8' VSCHP'	instruction name
00001958	00000010			1268+	DC	A(16)	result length
0000195C	00001980			1269+	DC	A(RE22)	address of expected result
				1270+*			
00001960				1271+X22	DS	0F	
00001960	E710 8F2C 0006		0000112C	1272+	VL	V1, V1FUDGE	fudge V1
00001966	E320 500C 0014		0000194C	1273+	LGF	R2, V2_22	get v2
0000196C	E722 0000 0006		00000000	1274+	VL	V2, 0(R2)	
00001972	E730 5009 7000		00001949	1275+	VLEB	V3, V3_22, 7	get v3 scale
00001978	E612 3000 4074			1276+	VSCHP	V1, V2, V3, 4, 0	test instruction
0000197E	07FB			1277+	BR	R11	return
00001980				1278+RE22	DS	0F	expected 16 byte result
00001980				1279+	DROP	R5	
00001980	D0800000 00000000			1280	DC	XL16' D080000000000000C200000000000000'	
00001988	C2000000 00000000						
00001990	00000000 00009223			1281	DC	XL16' 00000000000009223372036854775808D'	
00001998	37203685 4775808D						
				1282			
				1283 *	9223372036854775807		
				1284	VRR_B	VSCHP, 4, 0, 0	
000019A0				1285+	DS	0FD	
000019A0		000019A0		1286+	USING	*, R5	base for test data and test routine
000019A0	000019C0			1287+T23	DC	A(X23)	address of test routine
000019A4	0017			1288+	DC	H' 23'	test number
000019A6	00			1289+	DC	X' 00'	
000019A7	04			1290+	DC	HL1' 4'	m4
000019A8	00			1291+	DC	HL1' 0'	m5
000019A9	00			1292+V3_23	DC	HL1' 0'	scale
000019AC	000019F0			1293+V2_23	DC	A(RE23+16)	address of v2: 16-byte packed decimal
000019B0	E5E2C3C8 D7404040			1294+	DC	CL8' VSCHP'	instruction name
000019B8	00000010			1295+	DC	A(16)	result length
000019BC	000019E0			1296+	DC	A(RE23)	address of expected result
				1297+*			
000019C0				1298+X23	DS	0F	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000019C0	E710 8F2C 0006		0000112C	1299+	VL	V1, V1FUDGE	fudge V1
000019C6	E320 500C 0014		000019AC	1300+	LGF	R2, V2_23	get v2
000019CC	E722 0000 0006		00000000	1301+	VL	V2, 0(R2)	
000019D2	E730 5009 7000		000019A9	1302+	VLEB	V3, V3_23, 7	get v3 scale
000019D8	E612 3000 4074			1303+	VSCHP	V1, V2, V3, 4, 0	test instruction
000019DE	07FB			1304+	BR	R11	return
000019E0				1305+RE23	DS	0F	expected 16 byte result
000019E0				1306+	DROP	R5	
000019E0	507FFFFFFF FFFFFFFF			1307	DC	XL16' 507FFFFFFF42FF000000000000'	
000019E8	42FF0000 00000000						
000019F0	00000000 00009223			1308	DC	XL16' 0000000000009223372036854775807C'	
000019F8	37203685 4775807C						
				1309			
				1310 *		18446744073709551615	
00001A00				1311	VRR_B	VSCHP, 4, 0, 0	
00001A00		00001A00		1312+	DS	0FD	
00001A00	00001A20			1313+	USING	*, R5	base for test data and test routine
00001A04	0018			1314+T24	DC	A(X24)	address of test routine
00001A06	00			1315+	DC	H' 24'	test number
00001A06	00			1316+	DC	X' 00'	
00001A07	04			1317+	DC	HL1' 4'	m4
00001A08	00			1318+	DC	HL1' 0'	m5
00001A09	00			1319+V3_24	DC	HL1' 0'	scale
00001A0C	00001A50			1320+V2_24	DC	A(RE24+16)	address of v2: 16-byte packed decimal
00001A10	E5E2C3C8 D7404040			1321+	DC	CL8' VSCHP'	instruction name
00001A18	00000010			1322+	DC	A(16)	result length
00001A1C	00001A40			1323+	DC	A(RE24)	address of expected result
				1324+*			
00001A20				1325+X24	DS	0F	
00001A20	E710 8F2C 0006		0000112C	1326+	VL	V1, V1FUDGE	fudge V1
00001A26	E320 500C 0014		00001A0C	1327+	LGF	R2, V2_24	get v2
00001A2C	E722 0000 0006		00000000	1328+	VL	V2, 0(R2)	
00001A32	E730 5009 7000		00001A09	1329+	VLEB	V3, V3_24, 7	get v3 scale
00001A38	E612 3000 4074			1330+	VSCHP	V1, V2, V3, 4, 0	test instruction
00001A3E	07FB			1331+	BR	R11	return
00001A40				1332+RE24	DS	0F	expected 16 byte result
00001A40				1333+	DROP	R5	
00001A40	50FFFFFF FFFFFFFF			1334	DC	XL16' 50FFFFFF42FF000000000000'	
00001A48	42FF0000 00000000						
00001A50	00000000 00018446			1335	DC	XL16' 0000000000018446744073709551615C'	
00001A58	74407370 9551615C						
				1336			
				1337 *		-----	
				1338 *		No Rounding - with shifts	
				1339 *		-----	
				1340 *		-----	
				1341 *		short float	
				1342 *		-----	
				1343 *		+0	
				1344	VRR_B	VSCHP, 2, 0, 1	
00001A60				1345+	DS	0FD	
00001A60		00001A60		1346+	USING	*, R5	base for test data and test routine
00001A60	00001A80			1347+T25	DC	A(X25)	address of test routine
00001A64	0019			1348+	DC	H' 25'	test number
00001A66	00			1349+	DC	X' 00'	
00001A67	02			1350+	DC	HL1' 2'	m4

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00001A68	00			1351+	DC	HL1' 0'
00001A69	01			1352+V3_25	DC	HL1' 1'
00001A6C	00001AB0			1353+V2_25	DC	A(RE25+16)
00001A70	E5E2C3C8 D7404040			1354+	DC	CL8' VSCHP'
00001A78	00000010			1355+	DC	A(16)
00001A7C	00001AA0			1356+	DC	A(RE25)
				1357+*		
00001A80				1358+X25	DS	0F
00001A80	E710 8F2C 0006		0000112C	1359+	VL	V1, V1FUDGE
00001A86	E320 500C 0014		00001A6C	1360+	LGF	R2, V2_25
00001A8C	E722 0000 0006		00000000	1361+	VL	V2, 0(R2)
00001A92	E730 5009 7000		00001A69	1362+	VLEB	V3, V3_25, 7
00001A98	E612 3000 2074			1363+	VSCHP	V1, V2, V3, 2, 0
00001A9E	07FB			1364+	BR	R11
00001AA0				1365+RE25	DS	0F
00001AA0				1366+	DROP	R5
00001AA0	00000000 00000000			1367	DC	XL16' 00000000000000000000000000000000'
00001AA8	00000000 00000000					
00001AB0	00000000 00000000			1368	DC	XL16' 00000000000000000000000000000000C'
00001AB8	00000000 0000000C					
				1369 * -0		
				1370	VRR_B	VSCHP, 2, 0, 1
00001AC0				1371+	DS	0FD
00001AC0		00001AC0		1372+	USING	*, R5
00001AC0	00001AE0			1373+T26	DC	A(X26)
00001AC4	001A			1374+	DC	H' 26'
00001AC6	00			1375+	DC	X' 00'
00001AC7	02			1376+	DC	HL1' 2'
00001AC8	00			1377+	DC	HL1' 0'
00001AC9	01			1378+V3_26	DC	HL1' 1'
00001ACC	00001B10			1379+V2_26	DC	A(RE26+16)
00001AD0	E5E2C3C8 D7404040			1380+	DC	CL8' VSCHP'
00001AD8	00000010			1381+	DC	A(16)
00001ADC	00001B00			1382+	DC	A(RE26)
				1383+*		
00001AE0				1384+X26	DS	0F
00001AE0	E710 8F2C 0006		0000112C	1385+	VL	V1, V1FUDGE
00001AE6	E320 500C 0014		00001ACC	1386+	LGF	R2, V2_26
00001AEC	E722 0000 0006		00000000	1387+	VL	V2, 0(R2)
00001AF2	E730 5009 7000		00001AC9	1388+	VLEB	V3, V3_26, 7
00001AF8	E612 3000 2074			1389+	VSCHP	V1, V2, V3, 2, 0
00001AFE	07FB			1390+	BR	R11
00001B00				1391+RE26	DS	0F
00001B00				1392+	DROP	R5
00001B00	00000000 00000000			1393	DC	XL16' 00000000000000000000000000000000'
00001B08	00000000 00000000					
00001B10	00000000 00000000			1394	DC	XL16' 00000000000000000000000000000000D'
00001B18	00000000 0000000D					
				1395 * +1		
				1396	VRR_B	VSCHP, 2, 0, 1
00001B20				1397+	DS	0FD
00001B20		00001B20		1398+	USING	*, R5
00001B20	00001B40			1399+T27	DC	A(X27)
00001B24	001B			1400+	DC	H' 27'
00001B26	00			1401+	DC	X' 00'
00001B27	02			1402+	DC	HL1' 2'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00001B28	00			1403+	DC	HL1' 0'
00001B29	01			1404+V3_27	DC	HL1' 1'
00001B2C	00001B70			1405+V2_27	DC	A(RE27+16)
00001B30	E5E2C3C8 D7404040			1406+	DC	CL8' VSCHP'
00001B38	00000010			1407+	DC	A(16)
00001B3C	00001B60			1408+	DC	A(RE27)
				1409+*		
00001B40				1410+X27	DS	0F
00001B40	E710 8F2C 0006		0000112C	1411+	VL	V1, V1FUDGE
00001B46	E320 500C 0014		00001B2C	1412+	LGF	R2, V2_27
00001B4C	E722 0000 0006		00000000	1413+	VL	V2, 0(R2)
00001B52	E730 5009 7000		00001B29	1414+	VLEB	V3, V3_27, 7
00001B58	E612 3000 2074			1415+	VSCHP	V1, V2, V3, 2, 0
00001B5E	07FB			1416+	BR	R11
00001B60				1417+RE27	DS	0F
00001B60				1418+	DROP	R5
00001B60	41A00000 00000000			1419	DC	XL16' 41A00000000000000000000000000000'
00001B68	00000000 00000000					
00001B70	00000000 00000000			1420	DC	XL16' 000000000000000000000000000000001C'
00001B78	00000000 0000001C					
				1421 * -1		
				1422	VRR_B	VSCHP, 2, 0, 1
00001B80				1423+	DS	0FD
00001B80		00001B80		1424+	USING	*, R5
00001B80	00001BA0			1425+T28	DC	A(X28)
00001B84	001C			1426+	DC	H' 28'
00001B86	00			1427+	DC	X' 00'
00001B87	02			1428+	DC	HL1' 2'
00001B88	00			1429+	DC	HL1' 0'
00001B89	01			1430+V3_28	DC	HL1' 1'
00001B8C	00001BD0			1431+V2_28	DC	A(RE28+16)
00001B90	E5E2C3C8 D7404040			1432+	DC	CL8' VSCHP'
00001B98	00000010			1433+	DC	A(16)
00001B9C	00001BC0			1434+	DC	A(RE28)
				1435+*		
00001BA0				1436+X28	DS	0F
00001BA0	E710 8F2C 0006		0000112C	1437+	VL	V1, V1FUDGE
00001BA6	E320 500C 0014		00001B8C	1438+	LGF	R2, V2_28
00001BAC	E722 0000 0006		00000000	1439+	VL	V2, 0(R2)
00001BB2	E730 5009 7000		00001B89	1440+	VLEB	V3, V3_28, 7
00001BB8	E612 3000 2074			1441+	VSCHP	V1, V2, V3, 2, 0
00001BBE	07FB			1442+	BR	R11
00001BC0				1443+RE28	DS	0F
00001BC0				1444+	DROP	R5
00001BC0	C1A00000 00000000			1445	DC	XL16' C1A00000000000000000000000000000'
00001BC8	00000000 00000000					
00001BD0	00000000 00000000			1446	DC	XL16' 000000000000000000000000000000001D'
00001BD8	00000000 0000001D					
				1447		
				1448 * +900000000000000001		
				1449	VRR_B	VSCHP, 2, 0, 2
00001BE0				1450+	DS	0FD
00001BE0		00001BE0		1451+	USING	*, R5
00001BE0	00001C00			1452+T29	DC	A(X29)
00001BE4	001D			1453+	DC	H' 29'
00001BE6	00			1454+	DC	X' 00'



LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00001CA4	001F			1507+	DC	H' 31' test number
00001CA6	00			1508+	DC	X' 00'
00001CA7	02			1509+	DC	HL1' 2' m4
00001CA8	00			1510+	DC	HL1' 0' m5
00001CA9	02			1511+V3_31	DC	HL1' 2' scale
00001CAC	00001CF0			1512+V2_31	DC	A(RE31+16) address of v2: 16-byte packed decimal
00001CB0	E5E2C3C8 D7404040			1513+	DC	CL8' VSCHP' instruction name
00001CB8	00000010			1514+	DC	A(16) result length
00001CBC	00001CE0			1515+	DC	A(RE31) address of expected result
				1516+*		
00001CC0				1517+X31	DS	0F
00001CC0	E710 8F2C 0006		0000112C	1518+	VL	V1, V1FUDGE fudge V1
00001CC6	E320 500C 0014		00001CAC	1519+	LGF	R2, V2_31 get v2
00001CCC	E722 0000 0006		00000000	1520+	VL	V2, 0(R2)
00001CD2	E730 5009 7000		00001CA9	1521+	VLEB	V3, V3_31, 7 get v3 scale
00001CD8	E612 3000 2074			1522+	VSCHP	V1, V2, V3, 2, 0 test instruction
00001CDE	07FB			1523+	BR	R11 return
00001CE0				1524+RE31	DS	0F expected 16 byte result
00001CE0				1525+	DROP	R5
00001CE0	5231FFFF 00000000			1526	DC	XL16' 5231FFFF000000000000000000000000'
00001CE8	00000000 00000000					
00001CF0	00000000 00009223			1527	DC	XL16' 00000000000009223372036854775807C'
00001CF8	37203685 4775807C					
				1528		
				1529 * 18446744073709551615		
				1530	VRR_B VSCHP, 2, 0, 2	
00001D00				1531+	DS	0FD
00001D00		00001D00		1532+	USING	*, R5 base for test data and test routine
00001D00	00001D20			1533+T32	DC	A(X32) address of test routine
00001D04	0020			1534+	DC	H' 32' test number
00001D06	00			1535+	DC	X' 00'
00001D07	02			1536+	DC	HL1' 2' m4
00001D08	00			1537+	DC	HL1' 0' m5
00001D09	02			1538+V3_32	DC	HL1' 2' scale
00001D0C	00001D50			1539+V2_32	DC	A(RE32+16) address of v2: 16-byte packed decimal
00001D10	E5E2C3C8 D7404040			1540+	DC	CL8' VSCHP' instruction name
00001D18	00000010			1541+	DC	A(16) result length
00001D1C	00001D40			1542+	DC	A(RE32) address of expected result
				1543+*		
00001D20				1544+X32	DS	0F
00001D20	E710 8F2C 0006		0000112C	1545+	VL	V1, V1FUDGE fudge V1
00001D26	E320 500C 0014		00001D0C	1546+	LGF	R2, V2_32 get v2
00001D2C	E722 0000 0006		00000000	1547+	VL	V2, 0(R2)
00001D32	E730 5009 7000		00001D09	1548+	VLEB	V3, V3_32, 7 get v3 scale
00001D38	E612 3000 2074			1549+	VSCHP	V1, V2, V3, 2, 0 test instruction
00001D3E	07FB			1550+	BR	R11 return
00001D40				1551+RE32	DS	0F expected 16 byte result
00001D40				1552+	DROP	R5
00001D40	5263FFFF 00000000			1553	DC	XL16' 5263FFFF000000000000000000000000'
00001D48	00000000 00000000					
00001D50	00000000 00018446			1554	DC	XL16' 0000000000018446744073709551615C'
00001D58	74407370 9551615C					
				1555		
				1556 *		-----
				1557 * long float		
				1558 *		-----

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
				1559 * +0		
				1560	VRR_B VSCHP, 3, 0, 1	
00001D60				1561+	DS OFD	
00001D60		00001D60		1562+	USING *, R5	base for test data and test routine
00001D60	00001D80			1563+T33	DC A(X33)	address of test routine
00001D64	0021			1564+	DC H' 33'	test number
00001D66	00			1565+	DC X' 00'	
00001D67	03			1566+	DC HL1' 3'	m4
00001D68	00			1567+	DC HL1' 0'	m5
00001D69	01			1568+V3_33	DC HL1' 1'	scale
00001D6C	00001DB0			1569+V2_33	DC A(RE33+16)	address of v2: 16-byte packed decimal
00001D70	E5E2C3C8 D7404040			1570+	DC CL8' VSCHP'	instruction name
00001D78	00000010			1571+	DC A(16)	result length
00001D7C	00001DA0			1572+	DC A(RE33)	address of expected result
				1573+*		
00001D80				1574+X33	DS OF	
00001D80	E710 8F2C 0006		0000112C	1575+	VL V1, V1FUDGE	fudge V1
00001D86	E320 500C 0014		00001D6C	1576+	LGF R2, V2_33	get v2
00001D8C	E722 0000 0006		00000000	1577+	VL V2, 0(R2)	
00001D92	E730 5009 7000		00001D69	1578+	VLEB V3, V3_33, 7	get v3 scale
00001D98	E612 3000 3074			1579+	VSCHP V1, V2, V3, 3, 0	test instruction
00001D9E	07FB			1580+	BR R11	return
00001DA0				1581+RE33	DS OF	expected 16 byte result
00001DA0				1582+	DROP R5	
00001DA0	00000000 00000000			1583	DC XL16' 00000000000000000000000000000000'	
00001DA8	00000000 00000000					
00001DB0	00000000 00000000			1584	DC XL16' 00000000000000000000000000000000C'	
00001DB8	00000000 0000000C					
				1585 * -0		
				1586	VRR_B VSCHP, 3, 0, 1	
00001DC0				1587+	DS OFD	
00001DC0		00001DC0		1588+	USING *, R5	base for test data and test routine
00001DC0	00001DE0			1589+T34	DC A(X34)	address of test routine
00001DC4	0022			1590+	DC H' 34'	test number
00001DC6	00			1591+	DC X' 00'	
00001DC7	03			1592+	DC HL1' 3'	m4
00001DC8	00			1593+	DC HL1' 0'	m5
00001DC9	01			1594+V3_34	DC HL1' 1'	scale
00001DCC	00001E10			1595+V2_34	DC A(RE34+16)	address of v2: 16-byte packed decimal
00001DD0	E5E2C3C8 D7404040			1596+	DC CL8' VSCHP'	instruction name
00001DD8	00000010			1597+	DC A(16)	result length
00001DDC	00001E00			1598+	DC A(RE34)	address of expected result
				1599+*		
00001DE0				1600+X34	DS OF	
00001DE0	E710 8F2C 0006		0000112C	1601+	VL V1, V1FUDGE	fudge V1
00001DE6	E320 500C 0014		00001DCC	1602+	LGF R2, V2_34	get v2
00001DEC	E722 0000 0006		00000000	1603+	VL V2, 0(R2)	
00001DF2	E730 5009 7000		00001DC9	1604+	VLEB V3, V3_34, 7	get v3 scale
00001DF8	E612 3000 3074			1605+	VSCHP V1, V2, V3, 3, 0	test instruction
00001DFE	07FB			1606+	BR R11	return
00001E00				1607+RE34	DS OF	expected 16 byte result
00001E00				1608+	DROP R5	
00001E00	00000000 00000000			1609	DC XL16' 00000000000000000000000000000000'	
00001E08	00000000 00000000					
00001E10	00000000 00000000			1610	DC XL16' 00000000000000000000000000000000D'	
00001E18	00000000 0000000D					

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
				1611 * +1		
				1612	VRR_B VSCHP, 3, 0, 1	
00001E20				1613+	DS OFD	
00001E20		00001E20		1614+	USING *, R5	base for test data and test routine
00001E20	00001E40			1615+T35	DC A(X35)	address of test routine
00001E24	0023			1616+	DC H' 35'	test number
00001E26	00			1617+	DC X' 00'	
00001E27	03			1618+	DC HL1' 3'	m4
00001E28	00			1619+	DC HL1' 0'	m5
00001E29	01			1620+V3_35	DC HL1' 1'	scale
00001E2C	00001E70			1621+V2_35	DC A(RE35+16)	address of v2: 16-byte packed decimal
00001E30	E5E2C3C8 D7404040			1622+	DC CL8' VSCHP'	instruction name
00001E38	00000010			1623+	DC A(16)	result length
00001E3C	00001E60			1624+	DC A(RE35)	address of expected result
				1625+*		
00001E40				1626+X35	DS OF	
00001E40	E710 8F2C 0006		0000112C	1627+	VL V1, V1FUDGE	fudge V1
00001E46	E320 500C 0014		00001E2C	1628+	LGF R2, V2_35	get v2
00001E4C	E722 0000 0006		00000000	1629+	VL V2, 0(R2)	
00001E52	E730 5009 7000		00001E29	1630+	VLEB V3, V3_35, 7	get v3 scale
00001E58	E612 3000 3074			1631+	VSCHP V1, V2, V3, 3, 0	test instruction
00001E5E	07FB			1632+	BR R11	return
00001E60				1633+RE35	DS OF	expected 16 byte result
00001E60				1634+	DROP R5	
00001E60	41A00000 00000000			1635	DC XL16' 41A00000000000000000000000000000'	
00001E68	00000000 00000000					
00001E70	00000000 00000000			1636	DC XL16' 000000000000000000000000000000001C'	
00001E78	00000000 0000001C					
				1637 * -1		
				1638	VRR_B VSCHP, 3, 0, 1	
00001E80				1639+	DS OFD	
00001E80		00001E80		1640+	USING *, R5	base for test data and test routine
00001E80	00001EA0			1641+T36	DC A(X36)	address of test routine
00001E84	0024			1642+	DC H' 36'	test number
00001E86	00			1643+	DC X' 00'	
00001E87	03			1644+	DC HL1' 3'	m4
00001E88	00			1645+	DC HL1' 0'	m5
00001E89	01			1646+V3_36	DC HL1' 1'	scale
00001E8C	00001ED0			1647+V2_36	DC A(RE36+16)	address of v2: 16-byte packed decimal
00001E90	E5E2C3C8 D7404040			1648+	DC CL8' VSCHP'	instruction name
00001E98	00000010			1649+	DC A(16)	result length
00001E9C	00001EC0			1650+	DC A(RE36)	address of expected result
				1651+*		
00001EA0				1652+X36	DS OF	
00001EA0	E710 8F2C 0006		0000112C	1653+	VL V1, V1FUDGE	fudge V1
00001EA6	E320 500C 0014		00001E8C	1654+	LGF R2, V2_36	get v2
00001EAC	E722 0000 0006		00000000	1655+	VL V2, 0(R2)	
00001EB2	E730 5009 7000		00001E89	1656+	VLEB V3, V3_36, 7	get v3 scale
00001EB8	E612 3000 3074			1657+	VSCHP V1, V2, V3, 3, 0	test instruction
00001EBE	07FB			1658+	BR R11	return
00001EC0				1659+RE36	DS OF	expected 16 byte result
00001EC0				1660+	DROP R5	
00001EC0	C1A00000 00000000			1661	DC XL16' C1A00000000000000000000000000000'	
00001EC8	00000000 00000000					
00001ED0	00000000 00000000			1662	DC XL16' 000000000000000000000000000000001D'	
00001ED8	00000000 0000001D					



LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00001F90	00000000 00009223			1716	DC	XL16' 00000000000009223372036854775808D'
00001F98	37203685 4775808D					
				1717		
				1718 *		9223372036854775807
				1719	VRR_B	VSCHP, 3, 0, 2
00001FA0				1720+	DS	OFD
00001FA0		00001FA0		1721+	USING	*, R5
00001FA0	00001FC0			1722+T39	DC	A(X39)
00001FA4	0027			1723+	DC	H' 39'
00001FA6	00			1724+	DC	X' 00'
00001FA7	03			1725+	DC	HL1' 3'
00001FA8	00			1726+	DC	HL1' 0'
00001FA9	02			1727+V3_39	DC	HL1' 2'
00001FAC	00001FF0			1728+V2_39	DC	A(RE39+16)
00001FB0	E5E2C3C8 D7404040			1729+	DC	CL8' VSCHP'
00001FB8	00000010			1730+	DC	A(16)
00001FBC	00001FE0			1731+	DC	A(RE39)
				1732+*		
00001FC0				1733+X39	DS	OF
00001FC0	E710 8F2C 0006		0000112C	1734+	VL	V1, V1FUDGE
00001FC6	E320 500C 0014		00001FAC	1735+	LGF	R2, V2_39
00001FCC	E722 0000 0006		00000000	1736+	VL	V2, 0(R2)
00001FD2	E730 5009 7000		00001FA9	1737+	VLEB	V3, V3_39, 7
00001FD8	E612 3000 3074			1738+	VSCHP	V1, V2, V3, 3, 0
00001FDE	07FB			1739+	BR	R11
00001FE0				1740+RE39	DS	OF
00001FE0				1741+	DROP	R5
00001FE0	5231FFFF FFFFFFFF			1742	DC	XL16' 5231FFFFFFFFFFFFFFFF0000000000000000'
00001FE8	00000000 00000000					
00001FF0	00000000 00009223			1743	DC	XL16' 00000000000009223372036854775807C'
00001FF8	37203685 4775807C					
				1744		
				1745 *		18446744073709551615
				1746	VRR_B	VSCHP, 3, 0, 2
00002000				1747+	DS	OFD
00002000		00002000		1748+	USING	*, R5
00002000	00002020			1749+T40	DC	A(X40)
00002004	0028			1750+	DC	H' 40'
00002006	00			1751+	DC	X' 00'
00002007	03			1752+	DC	HL1' 3'
00002008	00			1753+	DC	HL1' 0'
00002009	02			1754+V3_40	DC	HL1' 2'
0000200C	00002050			1755+V2_40	DC	A(RE40+16)
00002010	E5E2C3C8 D7404040			1756+	DC	CL8' VSCHP'
00002018	00000010			1757+	DC	A(16)
0000201C	00002040			1758+	DC	A(RE40)
				1759+*		
00002020				1760+X40	DS	OF
00002020	E710 8F2C 0006		0000112C	1761+	VL	V1, V1FUDGE
00002026	E320 500C 0014		0000200C	1762+	LGF	R2, V2_40
0000202C	E722 0000 0006		00000000	1763+	VL	V2, 0(R2)
00002032	E730 5009 7000		00002009	1764+	VLEB	V3, V3_40, 7
00002038	E612 3000 3074			1765+	VSCHP	V1, V2, V3, 3, 0
0000203E	07FB			1766+	BR	R11
00002040				1767+RE40	DS	OF
00002040				1768+	DROP	R5

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00002040	5263FFFF FFFFFFFF			1769	DC	XL16' 5263FFFFFFFFFFFFFFFF0000000000000000'
00002048	00000000 00000000					
00002050	00000000 00018446			1770	DC	XL16' 00000000000018446744073709551615C'
00002058	74407370 9551615C					
				1771		
				1772	*	-----
				1773	*	extended float
				1774	*	-----
				1775	*	+0
				1776		VRR_B VSCHP, 4, 0, 1
00002060				1777+	DS	OFD
00002060		00002060		1778+	USING	*, R5
00002060	00002080			1779+T41	DC	A(X41)
00002064	0029			1780+	DC	H' 41'
00002066	00			1781+	DC	X' 00'
00002067	04			1782+	DC	HL1' 4'
00002068	00			1783+	DC	HL1' 0'
00002069	01			1784+V3_41	DC	HL1' 1'
0000206C	000020B0			1785+V2_41	DC	A(RE41+16)
00002070	E5E2C3C8 D7404040			1786+	DC	CL8' VSCHP'
00002078	00000010			1787+	DC	A(16)
0000207C	000020A0			1788+	DC	A(RE41)
				1789+*		
00002080				1790+X41	DS	OF
00002080	E710 8F2C 0006		0000112C	1791+	VL	V1, V1FUDGE
00002086	E320 500C 0014		0000206C	1792+	LGF	R2, V2_41
0000208C	E722 0000 0006		00000000	1793+	VL	V2, 0(R2)
00002092	E730 5009 7000		00002069	1794+	VLEB	V3, V3_41, 7
00002098	E612 3000 4074			1795+	VSCHP	V1, V2, V3, 4, 0
0000209E	07FB			1796+	BR	R11
000020A0				1797+RE41	DS	OF
000020A0				1798+	DROP	R5
000020A0	00000000 00000000			1799	DC	XL16' 00000000000000000000000000000000'
000020A8	00000000 00000000					
000020B0	00000000 00000000			1800	DC	XL16' 00000000000000000000000000000000C'
000020B8	00000000 0000000C					
				1801	*	- 0
				1802		VRR_B VSCHP, 4, 0, 1
000020C0				1803+	DS	OFD
000020C0		000020C0		1804+	USING	*, R5
000020C0	000020E0			1805+T42	DC	A(X42)
000020C4	002A			1806+	DC	H' 42'
000020C6	00			1807+	DC	X' 00'
000020C7	04			1808+	DC	HL1' 4'
000020C8	00			1809+	DC	HL1' 0'
000020C9	01			1810+V3_42	DC	HL1' 1'
000020CC	00002110			1811+V2_42	DC	A(RE42+16)
000020D0	E5E2C3C8 D7404040			1812+	DC	CL8' VSCHP'
000020D8	00000010			1813+	DC	A(16)
000020DC	00002100			1814+	DC	A(RE42)
				1815+*		
000020E0				1816+X42	DS	OF
000020E0	E710 8F2C 0006		0000112C	1817+	VL	V1, V1FUDGE
000020E6	E320 500C 0014		000020CC	1818+	LGF	R2, V2_42
000020EC	E722 0000 0006		00000000	1819+	VL	V2, 0(R2)
000020F2	E730 5009 7000		000020C9	1820+	VLEB	V3, V3_42, 7

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000020F8	E612 3000 4074			1821+	VSCHP	V1, V2, V3, 4, 0	test instruction
000020FE	07FB			1822+	BR	R11	return
00002100				1823+RE42	DS	0F	expected 16 byte result
00002100				1824+	DROP	R5	
00002100	00000000 00000000			1825	DC	XL16' 00000000000000000000000000000000'	
00002108	00000000 00000000						
00002110	00000000 00000000			1826	DC	XL16' 00000000000000000000000000000000D'	
00002118	00000000 0000000D						
				1827 * +1			
				1828	VRR_B	VSCHP, 4, 0, 1	
00002120				1829+	DS	0FD	
00002120		00002120		1830+	USING	*, R5	base for test data and test routine
00002120	00002140			1831+T43	DC	A(X43)	address of test routine
00002124	002B			1832+	DC	H' 43'	test number
00002126	00			1833+	DC	X' 00'	
00002127	04			1834+	DC	HL1' 4'	m4
00002128	00			1835+	DC	HL1' 0'	m5
00002129	01			1836+V3_43	DC	HL1' 1'	scale
0000212C	00002170			1837+V2_43	DC	A(RE43+16)	address of v2: 16-byte packed decimal
00002130	E5E2C3C8 D7404040			1838+	DC	CL8' VSCHP'	instruction name
00002138	00000010			1839+	DC	A(16)	result length
0000213C	00002160			1840+	DC	A(RE43)	address of expected result
				1841+*			
00002140				1842+X43	DS	0F	
00002140	E710 8F2C 0006		0000112C	1843+	VL	V1, V1FUDGE	fudge V1
00002146	E320 500C 0014		0000212C	1844+	LGF	R2, V2_43	get v2
0000214C	E722 0000 0006		00000000	1845+	VL	V2, 0(R2)	
00002152	E730 5009 7000		00002129	1846+	VLEB	V3, V3_43, 7	get v3 scale
00002158	E612 3000 4074			1847+	VSCHP	V1, V2, V3, 4, 0	test instruction
0000215E	07FB			1848+	BR	R11	return
00002160				1849+RE43	DS	0F	expected 16 byte result
00002160				1850+	DROP	R5	
00002160	41A00000 00000000			1851	DC	XL16' 41A0000000000000000330000000000000'	
00002168	33000000 00000000						
00002170	00000000 00000000			1852	DC	XL16' 000000000000000000000000000000001C'	
00002178	00000000 0000001C						
				1853 * -1			
				1854	VRR_B	VSCHP, 4, 0, 1	
00002180				1855+	DS	0FD	
00002180		00002180		1856+	USING	*, R5	base for test data and test routine
00002180	000021A0			1857+T44	DC	A(X44)	address of test routine
00002184	002C			1858+	DC	H' 44'	test number
00002186	00			1859+	DC	X' 00'	
00002187	04			1860+	DC	HL1' 4'	m4
00002188	00			1861+	DC	HL1' 0'	m5
00002189	01			1862+V3_44	DC	HL1' 1'	scale
0000218C	000021D0			1863+V2_44	DC	A(RE44+16)	address of v2: 16-byte packed decimal
00002190	E5E2C3C8 D7404040			1864+	DC	CL8' VSCHP'	instruction name
00002198	00000010			1865+	DC	A(16)	result length
0000219C	000021C0			1866+	DC	A(RE44)	address of expected result
				1867+*			
000021A0				1868+X44	DS	0F	
000021A0	E710 8F2C 0006		0000112C	1869+	VL	V1, V1FUDGE	fudge V1
000021A6	E320 500C 0014		0000218C	1870+	LGF	R2, V2_44	get v2
000021AC	E722 0000 0006		00000000	1871+	VL	V2, 0(R2)	
000021B2	E730 5009 7000		00002189	1872+	VLEB	V3, V3_44, 7	get v3 scale

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
000021B8	E612 3000 4074			1873+	VSCHP V1, V2, V3, 4, 0	test instruction
000021BE	07FB			1874+	BR R11	return
000021C0				1875+RE44	DS 0F	expected 16 byte result
000021C0				1876+	DROP R5	
000021C0	C1A00000 00000000			1877	DC XL16' C1A0000000000000B300000000000000'	
000021C8	B3000000 00000000					
000021D0	00000000 00000000			1878	DC XL16' 00000000000000000000000000000001D'	
000021D8	00000000 0000001D					
				1879		
				1880 *	+900000000000000001	
				1881	VRR_B VSCHP, 4, 0, 2	
000021E0				1882+	DS 0FD	
000021E0		000021E0		1883+	USING *, R5	base for test data and test routine
000021E0	00002200			1884+T45	DC A(X45)	address of test routine
000021E4	002D			1885+	DC H' 45'	test number
000021E6	00			1886+	DC X' 00'	
000021E7	04			1887+	DC HL1' 4'	m4
000021E8	00			1888+	DC HL1' 0'	m5
000021E9	02			1889+V3_45	DC HL1' 2'	scale
000021EC	00002230			1890+V2_45	DC A(RE45+16)	address of v2: 16-byte packed decimal
000021F0	E5E2C3C8 D7404040			1891+	DC CL8' VSCHP'	instruction name
000021F8	00000010			1892+	DC A(16)	result length
000021FC	00002220			1893+	DC A(RE45)	address of expected result
				1894+*		
00002200				1895+X45	DS 0F	
00002200	E710 8F2C 0006		0000112C	1896+	VL V1, V1FUDGE	fudge V1
00002206	E320 500C 0014		000021EC	1897+	LGF R2, V2_45	get v2
0000220C	E722 0000 0006		00000000	1898+	VL V2, 0(R2)	
00002212	E730 5009 7000		000021E9	1899+	VLEB V3, V3_45, 7	get v3 scale
00002218	E612 3000 4074			1900+	VSCHP V1, V2, V3, 4, 0	test instruction
0000221E	07FB			1901+	BR R11	return
00002220				1902+RE45	DS 0F	expected 16 byte result
00002220				1903+	DROP R5	
00002220	4FC7D713 B49DA006			1904	DC XL16' 4FC7D713B49DA0064140000000000000'	
00002228	41400000 00000000					
00002230	00000000 00000009			1905	DC XL16' 00000000000000009000000000000001C'	
00002238	00000000 0000001C					
				1906		
				1907 *	-9223372036854775808	
				1908	VRR_B VSCHP, 4, 0, 2	
00002240				1909+	DS 0FD	
00002240		00002240		1910+	USING *, R5	base for test data and test routine
00002240	00002260			1911+T46	DC A(X46)	address of test routine
00002244	002E			1912+	DC H' 46'	test number
00002246	00			1913+	DC X' 00'	
00002247	04			1914+	DC HL1' 4'	m4
00002248	00			1915+	DC HL1' 0'	m5
00002249	02			1916+V3_46	DC HL1' 2'	scale
0000224C	00002290			1917+V2_46	DC A(RE46+16)	address of v2: 16-byte packed decimal
00002250	E5E2C3C8 D7404040			1918+	DC CL8' VSCHP'	instruction name
00002258	00000010			1919+	DC A(16)	result length
0000225C	00002280			1920+	DC A(RE46)	address of expected result
				1921+*		
00002260				1922+X46	DS 0F	
00002260	E710 8F2C 0006		0000112C	1923+	VL V1, V1FUDGE	fudge V1
00002266	E320 500C 0014		0000224C	1924+	LGF R2, V2_46	get v2

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0000226C	E722 0000 0006		00000000	1925+	VL	V2, 0(R2)	
00002272	E730 5009 7000		00002249	1926+	VLEB	V3, V3_46, 7	get v3 scale
00002278	E612 3000 4074			1927+	VSCHP	V1, V2, V3, 4, 0	test instruction
0000227E	07FB			1928+	BR	R11	return
00002280				1929+RE46	DS	0F	expected 16 byte result
00002280				1930+	DROP	R5	
00002280	D2320000 00000000			1931	DC	XL16' D232000000000000C400000000000000'	
00002288	C4000000 00000000						
00002290	00000000 00009223			1932	DC	XL16' 00000000000009223372036854775808D'	
00002298	37203685 4775808D						
				1933			
				1934 *		9223372036854775807	
				1935	VRR_B	VSCHP, 4, 0, 2	
000022A0				1936+	DS	0FD	
000022A0		000022A0		1937+	USING	*, R5	base for test data and test routine
000022A0	000022C0			1938+T47	DC	A(X47)	address of test routine
000022A4	002F			1939+	DC	H' 47'	test number
000022A6	00			1940+	DC	X' 00'	
000022A7	04			1941+	DC	HL1' 4'	m4
000022A8	00			1942+	DC	HL1' 0'	m5
000022A9	02			1943+V3_47	DC	HL1' 2'	scale
000022AC	000022F0			1944+V2_47	DC	A(RE47+16)	address of v2: 16-byte packed decimal
000022B0	E5E2C3C8 D7404040			1945+	DC	CL8' VSCHP'	instruction name
000022B8	00000010			1946+	DC	A(16)	result length
000022BC	000022E0			1947+	DC	A(RE47)	address of expected result
				1948+*			
000022C0				1949+X47	DS	0F	
000022C0	E710 8F2C 0006		0000112C	1950+	VL	V1, V1FUDGE	fudge V1
000022C6	E320 500C 0014		000022AC	1951+	LGF	R2, V2_47	get v2
000022CC	E722 0000 0006		00000000	1952+	VL	V2, 0(R2)	
000022D2	E730 5009 7000		000022A9	1953+	VLEB	V3, V3_47, 7	get v3 scale
000022D8	E612 3000 4074			1954+	VSCHP	V1, V2, V3, 4, 0	test instruction
000022DE	07FB			1955+	BR	R11	return
000022E0				1956+RE47	DS	0F	expected 16 byte result
000022E0				1957+	DROP	R5	
000022E0	5231FFFF FFFFFFFF			1958	DC	XL16' 5231FFFFFFFFFFFFFFF44FF9C0000000000'	
000022E8	44FF9C00 00000000						
000022F0	00000000 00009223			1959	DC	XL16' 00000000000009223372036854775807C'	
000022F8	37203685 4775807C						
				1960			
				1961 *		18446744073709551615	
				1962	VRR_B	VSCHP, 4, 0, 2	
00002300				1963+	DS	0FD	
00002300		00002300		1964+	USING	*, R5	base for test data and test routine
00002300	00002320			1965+T48	DC	A(X48)	address of test routine
00002304	0030			1966+	DC	H' 48'	test number
00002306	00			1967+	DC	X' 00'	
00002307	04			1968+	DC	HL1' 4'	m4
00002308	00			1969+	DC	HL1' 0'	m5
00002309	02			1970+V3_48	DC	HL1' 2'	scale
0000230C	00002350			1971+V2_48	DC	A(RE48+16)	address of v2: 16-byte packed decimal
00002310	E5E2C3C8 D7404040			1972+	DC	CL8' VSCHP'	instruction name
00002318	00000010			1973+	DC	A(16)	result length
0000231C	00002340			1974+	DC	A(RE48)	address of expected result
				1975+*			
00002320				1976+X48	DS	0F	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002320	E710 8F2C 0006		0000112C	1977+	VL	V1, V1FUDGE	fudge V1
00002326	E320 500C 0014		0000230C	1978+	LGF	R2, V2_48	get v2
0000232C	E722 0000 0006		00000000	1979+	VL	V2, 0(R2)	
00002332	E730 5009 7000		00002309	1980+	VLEB	V3, V3_48, 7	get v3 scale
00002338	E612 3000 4074			1981+	VSCHP	V1, V2, V3, 4, 0	test instruction
0000233E	07FB			1982+	BR	R11	return
00002340				1983+RE48	DS	0F	expected 16 byte result
00002340				1984+	DROP	R5	
00002340	5263FFFF FFFFFFFF			1985	DC	XL16' 5263FFFFFFFFFFFFFFFF44FF9C000000000000'	
00002348	44FF9C00 00000000						
00002350	00000000 00018446			1986	DC	XL16' 00000000000018446744073709551615C'	
00002358	74407370 9551615C						
				1987			
				1988 *			
				1989 * ROUND - NO Shift			
				1990 *			
				1991 *			
				1992 * short float			
				1993 *			
				1994 * +0			
				1995	VRR_B	VSCHP, 2, 1, 0	
00002360				1996+	DS	0FD	
00002360		00002360		1997+	USING	*, R5	base for test data and test routine
00002360	00002380			1998+T49	DC	A(X49)	address of test routine
00002364	0031			1999+	DC	H' 49'	test number
00002366	00			2000+	DC	X' 00'	
00002367	02			2001+	DC	HL1' 2'	m4
00002368	01			2002+	DC	HL1' 1'	m5
00002369	00			2003+V3_49	DC	HL1' 0'	scale
0000236C	000023B0			2004+V2_49	DC	A(RE49+16)	address of v2: 16-byte packed decimal
00002370	E5E2C3C8 D7404040			2005+	DC	CL8' VSCHP'	instruction name
00002378	00000010			2006+	DC	A(16)	result length
0000237C	000023A0			2007+	DC	A(RE49)	address of expected result
				2008+*			
00002380				2009+X49	DS	0F	
00002380	E710 8F2C 0006		0000112C	2010+	VL	V1, V1FUDGE	fudge V1
00002386	E320 500C 0014		0000236C	2011+	LGF	R2, V2_49	get v2
0000238C	E722 0000 0006		00000000	2012+	VL	V2, 0(R2)	
00002392	E730 5009 7000		00002369	2013+	VLEB	V3, V3_49, 7	get v3 scale
00002398	E612 3010 2074			2014+	VSCHP	V1, V2, V3, 2, 1	test instruction
0000239E	07FB			2015+	BR	R11	return
000023A0				2016+RE49	DS	0F	expected 16 byte result
000023A0				2017+	DROP	R5	
000023A0	00000000 00000000			2018	DC	XL16' 00000000000000000000000000000000'	
000023A8	00000000 00000000						
000023B0	00000000 00000000			2019	DC	XL16' 00000000000000000000000000000000C'	
000023B8	00000000 0000000C						
				2020 * - 0			
				2021	VRR_B	VSCHP, 2, 1, 0	
000023C0				2022+	DS	0FD	
000023C0		000023C0		2023+	USING	*, R5	base for test data and test routine
000023C0	000023E0			2024+T50	DC	A(X50)	address of test routine
000023C4	0032			2025+	DC	H' 50'	test number
000023C6	00			2026+	DC	X' 00'	
000023C7	02			2027+	DC	HL1' 2'	m4
000023C8	01			2028+	DC	HL1' 1'	m5

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
000023C9	00			2029+V3_50	DC	HL1' 0' scale
000023CC	00002410			2030+V2_50	DC	A(RE50+16) address of v2: 16-byte packed decimal
000023D0	E5E2C3C8 D7404040			2031+	DC	CL8' VSCHP' instruction name
000023D8	00000010			2032+	DC	A(16) result length
000023DC	00002400			2033+	DC	A(RE50) address of expected result
				2034+*		
000023E0				2035+X50	DS	0F
000023E0	E710 8F2C 0006		0000112C	2036+	VL	V1, V1FUDGE fudge V1
000023E6	E320 500C 0014		000023CC	2037+	LGF	R2, V2_50 get v2
000023EC	E722 0000 0006		00000000	2038+	VL	V2, 0(R2)
000023F2	E730 5009 7000		000023C9	2039+	VLEB	V3, V3_50, 7 get v3 scale
000023F8	E612 3010 2074			2040+	VSCHP	V1, V2, V3, 2, 1 test instruction
000023FE	07FB			2041+	BR	R11 return
00002400				2042+RE50	DS	0F expected 16 byte result
00002400				2043+	DROP	R5
00002400	00000000 00000000			2044	DC	XL16' 00000000000000000000000000000000'
00002408	00000000 00000000					
00002410	00000000 00000000			2045	DC	XL16' 00000000000000000000000000000000D'
00002418	00000000 0000000D					
				2046 * +1		
				2047	VRR_B	VSCHP, 2, 1, 0
00002420				2048+	DS	0FD
00002420		00002420		2049+	USING	*, R5 base for test data and test routine
00002420	00002440			2050+T51	DC	A(X51) address of test routine
00002424	0033			2051+	DC	H' 51' test number
00002426	00			2052+	DC	X' 00'
00002427	02			2053+	DC	HL1' 2' m4
00002428	01			2054+	DC	HL1' 1' m5
00002429	00			2055+V3_51	DC	HL1' 0' scale
0000242C	00002470			2056+V2_51	DC	A(RE51+16) address of v2: 16-byte packed decimal
00002430	E5E2C3C8 D7404040			2057+	DC	CL8' VSCHP' instruction name
00002438	00000010			2058+	DC	A(16) result length
0000243C	00002460			2059+	DC	A(RE51) address of expected result
				2060+*		
00002440				2061+X51	DS	0F
00002440	E710 8F2C 0006		0000112C	2062+	VL	V1, V1FUDGE fudge V1
00002446	E320 500C 0014		0000242C	2063+	LGF	R2, V2_51 get v2
0000244C	E722 0000 0006		00000000	2064+	VL	V2, 0(R2)
00002452	E730 5009 7000		00002429	2065+	VLEB	V3, V3_51, 7 get v3 scale
00002458	E612 3010 2074			2066+	VSCHP	V1, V2, V3, 2, 1 test instruction
0000245E	07FB			2067+	BR	R11 return
00002460				2068+RE51	DS	0F expected 16 byte result
00002460				2069+	DROP	R5
00002460	41100000 00000000			2070	DC	XL16' 41100000000000000000000000000000'
00002468	00000000 00000000					
00002470	00000000 00000000			2071	DC	XL16' 000000000000000000000000000000001C'
00002478	00000000 0000001C					
				2072 * -1		
				2073	VRR_B	VSCHP, 2, 1, 0
00002480				2074+	DS	0FD
00002480		00002480		2075+	USING	*, R5 base for test data and test routine
00002480	000024A0			2076+T52	DC	A(X52) address of test routine
00002484	0034			2077+	DC	H' 52' test number
00002486	00			2078+	DC	X' 00'
00002487	02			2079+	DC	HL1' 2' m4
00002488	01			2080+	DC	HL1' 1' m5



LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002547	02			2133+	DC	HL1' 2'	m4
00002548	01			2134+	DC	HL1' 1'	m5
00002549	00			2135+V3_54	DC	HL1' 0'	scale
0000254C	00002590			2136+V2_54	DC	A(RE54+16)	address of v2: 16-byte packed decimal
00002550	E5E2C3C8 D7404040			2137+	DC	CL8' VSCHP'	instruction name
00002558	00000010			2138+	DC	A(16)	result length
0000255C	00002580			2139+	DC	A(RE54)	address of expected result
				2140+*			
00002560				2141+X54	DS	OF	
00002560	E710 8F2C 0006		0000112C	2142+	VL	V1, V1FUDGE	fudge V1
00002566	E320 500C 0014		0000254C	2143+	LGF	R2, V2_54	get v2
0000256C	E722 0000 0006		00000000	2144+	VL	V2, 0(R2)	
00002572	E730 5009 7000		00002549	2145+	VLEB	V3, V3_54, 7	get v3 scale
00002578	E612 3010 2074			2146+	VSCHP	V1, V2, V3, 2, 1	test instruction
0000257E	07FB			2147+	BR	R11	return
00002580				2148+RE54	DS	OF	expected 16 byte result
00002580				2149+	DROP	R5	
00002580	D0800000 00000000			2150	DC	XL16' D0800000000000000000000000000000'	
00002588	00000000 00000000						
00002590	00000000 00009223			2151	DC	XL16' 00000000000009223372036854775808D'	
00002598	37203685 4775808D						
				2152			
				2153 *		9223372036854775807	
000025A0				2154	VRR_B	VSCHP, 2, 1, 0	
000025A0		000025A0		2155+	DS	OFD	
000025A0	000025C0			2156+	USING	*, R5	base for test data and test routine
000025A4	0037			2157+T55	DC	A(X55)	address of test routine
000025A6	00			2158+	DC	H' 55'	test number
000025A7	02			2159+	DC	X' 00'	
000025A8	01			2160+	DC	HL1' 2'	m4
000025A9	00			2161+	DC	HL1' 1'	m5
000025AC	000025F0			2162+V3_55	DC	HL1' 0'	scale
000025B0	E5E2C3C8 D7404040			2163+V2_55	DC	A(RE55+16)	address of v2: 16-byte packed decimal
000025B8	00000010			2164+	DC	CL8' VSCHP'	instruction name
000025BC	000025E0			2165+	DC	A(16)	result length
				2166+	DC	A(RE55)	address of expected result
				2167+*			
000025C0				2168+X55	DS	OF	
000025C0	E710 8F2C 0006		0000112C	2169+	VL	V1, V1FUDGE	fudge V1
000025C6	E320 500C 0014		000025AC	2170+	LGF	R2, V2_55	get v2
000025CC	E722 0000 0006		00000000	2171+	VL	V2, 0(R2)	
000025D2	E730 5009 7000		000025A9	2172+	VLEB	V3, V3_55, 7	get v3 scale
000025D8	E612 3010 2074			2173+	VSCHP	V1, V2, V3, 2, 1	test instruction
000025DE	07FB			2174+	BR	R11	return
000025E0				2175+RE55	DS	OF	expected 16 byte result
000025E0				2176+	DROP	R5	
000025E0	50800000 00000000			2177	DC	XL16' 50800000000000000000000000000000'	
000025E8	00000000 00000000						
000025F0	00000000 00009223			2178	DC	XL16' 00000000000009223372036854775807C'	
000025F8	37203685 4775807C						
				2179			
				2180 *		18446744073709551615	
00002600				2181	VRR_B	VSCHP, 2, 1, 0	
00002600		00002600		2182+	DS	OFD	
00002600	00002620			2183+	USING	*, R5	base for test data and test routine
				2184+T56	DC	A(X56)	address of test routine

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00002604	0038			2185+	DC	H' 56'
00002606	00			2186+	DC	X' 00'
00002607	02			2187+	DC	HL1' 2'
00002608	01			2188+	DC	HL1' 1'
00002609	00			2189+V3_56	DC	HL1' 0'
0000260C	00002650			2190+V2_56	DC	A(RE56+16)
00002610	E5E2C3C8 D7404040			2191+	DC	CL8' VSCHP'
00002618	00000010			2192+	DC	A(16)
0000261C	00002640			2193+	DC	A(RE56)
				2194+*		
00002620				2195+X56	DS	0F
00002620	E710 8F2C 0006		0000112C	2196+	VL	V1, V1FUDGE
00002626	E320 500C 0014		0000260C	2197+	LGF	R2, V2_56
0000262C	E722 0000 0006		00000000	2198+	VL	V2, 0(R2)
00002632	E730 5009 7000		00002609	2199+	VLEB	V3, V3_56, 7
00002638	E612 3010 2074			2200+	VSCHP	V1, V2, V3, 2, 1
0000263E	07FB			2201+	BR	R11
00002640				2202+RE56	DS	0F
00002640				2203+	DROP	R5
00002640	51100000 00000000			2204	DC	XL16' 51100000000000000000000000000000'
00002648	00000000 00000000					
00002650	00000000 00018446			2205	DC	XL16' 00000000000018446744073709551615C'
00002658	74407370 9551615C					
				2206		
				2207 *		-----
				2208 *		long float
				2209 *		-----
				2210 *		+0
				2211	VRR_B	VSCHP, 3, 1, 0
00002660				2212+	DS	0FD
00002660		00002660		2213+	USING	*, R5
00002660	00002680			2214+T57	DC	A(X57)
00002664	0039			2215+	DC	H' 57'
00002666	00			2216+	DC	X' 00'
00002667	03			2217+	DC	HL1' 3'
00002668	01			2218+	DC	HL1' 1'
00002669	00			2219+V3_57	DC	HL1' 0'
0000266C	000026B0			2220+V2_57	DC	A(RE57+16)
00002670	E5E2C3C8 D7404040			2221+	DC	CL8' VSCHP'
00002678	00000010			2222+	DC	A(16)
0000267C	000026A0			2223+	DC	A(RE57)
				2224+*		
00002680				2225+X57	DS	0F
00002680	E710 8F2C 0006		0000112C	2226+	VL	V1, V1FUDGE
00002686	E320 500C 0014		0000266C	2227+	LGF	R2, V2_57
0000268C	E722 0000 0006		00000000	2228+	VL	V2, 0(R2)
00002692	E730 5009 7000		00002669	2229+	VLEB	V3, V3_57, 7
00002698	E612 3010 3074			2230+	VSCHP	V1, V2, V3, 3, 1
0000269E	07FB			2231+	BR	R11
000026A0				2232+RE57	DS	0F
000026A0				2233+	DROP	R5
000026A0	00000000 00000000			2234	DC	XL16' 00000000000000000000000000000000'
000026A8	00000000 00000000					
000026B0	00000000 00000000			2235	DC	XL16' 00000000000000000000000000000000C'
000026B8	00000000 0000000C					
				2236 *		- 0

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
000026C0				2237	VRR_B VSCHP, 3, 1, 0	
000026C0				2238+	DS OFD	
000026C0		000026C0		2239+	USING *, R5	base for test data and test routine
000026C0	000026E0			2240+T58	DC A(X58)	address of test routine
000026C4	003A			2241+	DC H' 58'	test number
000026C6	00			2242+	DC X' 00'	
000026C7	03			2243+	DC HL1' 3'	m4
000026C8	01			2244+	DC HL1' 1'	m5
000026C9	00			2245+V3_58	DC HL1' 0'	scale
000026CC	00002710			2246+V2_58	DC A(RE58+16)	address of v2: 16-byte packed decimal
000026D0	E5E2C3C8 D7404040			2247+	DC CL8' VSCHP'	instruction name
000026D8	00000010			2248+	DC A(16)	result length
000026DC	00002700			2249+	DC A(RE58)	address of expected result
				2250+*		
000026E0				2251+X58	DS OF	
000026E0	E710 8F2C 0006		0000112C	2252+	VL V1, V1FUDGE	fudge V1
000026E6	E320 500C 0014		000026CC	2253+	LGF R2, V2_58	get v2
000026EC	E722 0000 0006		00000000	2254+	VL V2, 0(R2)	
000026F2	E730 5009 7000		000026C9	2255+	VLEB V3, V3_58, 7	get v3 scale
000026F8	E612 3010 3074			2256+	VSCHP V1, V2, V3, 3, 1	test instruction
000026FE	07FB			2257+	BR R11	return
00002700				2258+RE58	DS OF	expected 16 byte result
00002700				2259+	DROP R5	
00002700	00000000 00000000			2260	DC XL16' 00000000000000000000000000000000'	
00002708	00000000 00000000					
00002710	00000000 00000000			2261	DC XL16' 00000000000000000000000000000000D'	
00002718	00000000 0000000D					
				2262 * +1		
				2263	VRR_B VSCHP, 3, 1, 0	
00002720				2264+	DS OFD	
00002720		00002720		2265+	USING *, R5	base for test data and test routine
00002720	00002740			2266+T59	DC A(X59)	address of test routine
00002724	003B			2267+	DC H' 59'	test number
00002726	00			2268+	DC X' 00'	
00002727	03			2269+	DC HL1' 3'	m4
00002728	01			2270+	DC HL1' 1'	m5
00002729	00			2271+V3_59	DC HL1' 0'	scale
0000272C	00002770			2272+V2_59	DC A(RE59+16)	address of v2: 16-byte packed decimal
00002730	E5E2C3C8 D7404040			2273+	DC CL8' VSCHP'	instruction name
00002738	00000010			2274+	DC A(16)	result length
0000273C	00002760			2275+	DC A(RE59)	address of expected result
				2276+*		
00002740				2277+X59	DS OF	
00002740	E710 8F2C 0006		0000112C	2278+	VL V1, V1FUDGE	fudge V1
00002746	E320 500C 0014		0000272C	2279+	LGF R2, V2_59	get v2
0000274C	E722 0000 0006		00000000	2280+	VL V2, 0(R2)	
00002752	E730 5009 7000		00002729	2281+	VLEB V3, V3_59, 7	get v3 scale
00002758	E612 3010 3074			2282+	VSCHP V1, V2, V3, 3, 1	test instruction
0000275E	07FB			2283+	BR R11	return
00002760				2284+RE59	DS OF	expected 16 byte result
00002760				2285+	DROP R5	
00002760	41100000 00000000			2286	DC XL16' 41100000000000000000000000000000'	
00002768	00000000 00000000					
00002770	00000000 00000000			2287	DC XL16' 000000000000000000000000000000001C'	
00002778	00000000 0000001C					
				2288 * -1		



LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				2341	
				2342 *	- 9223372036854775808
				2343	VRR_B VSCHP, 3, 1, 0
00002840				2344+	DS OFD
00002840		00002840		2345+	USING *, R5
00002840	00002860			2346+T62	DC A(X62)
00002844	003E			2347+	DC H' 62'
00002846	00			2348+	DC X' 00'
00002847	03			2349+	DC HL1' 3'
00002848	01			2350+	DC HL1' 1'
00002849	00			2351+V3_62	DC HL1' 0'
0000284C	00002890			2352+V2_62	DC A(RE62+16)
00002850	E5E2C3C8 D7404040			2353+	DC CL8' VSCHP'
00002858	00000010			2354+	DC A(16)
0000285C	00002880			2355+	DC A(RE62)
				2356+*	
00002860				2357+X62	DS OF
00002860	E710 8F2C 0006		0000112C	2358+	VL V1, V1FUDGE
00002866	E320 500C 0014		0000284C	2359+	LGF R2, V2_62
0000286C	E722 0000 0006		00000000	2360+	VL V2, 0(R2)
00002872	E730 5009 7000		00002849	2361+	VLEB V3, V3_62, 7
00002878	E612 3010 3074			2362+	VSCHP V1, V2, V3, 3, 1
0000287E	07FB			2363+	BR R11
00002880				2364+RE62	DS OF
00002880				2365+	DROP R5
00002880	D0800000 00000000			2366	DC XL16' D0800000000000000000000000000000'
00002888	00000000 00000000				
00002890	00000000 00009223			2367	DC XL16' 00000000000009223372036854775808D'
00002898	37203685 4775808D				
				2368	
				2369 *	9223372036854775807
				2370	VRR_B VSCHP, 3, 1, 0
000028A0				2371+	DS OFD
000028A0		000028A0		2372+	USING *, R5
000028A0	000028C0			2373+T63	DC A(X63)
000028A4	003F			2374+	DC H' 63'
000028A6	00			2375+	DC X' 00'
000028A7	03			2376+	DC HL1' 3'
000028A8	01			2377+	DC HL1' 1'
000028A9	00			2378+V3_63	DC HL1' 0'
000028AC	000028F0			2379+V2_63	DC A(RE63+16)
000028B0	E5E2C3C8 D7404040			2380+	DC CL8' VSCHP'
000028B8	00000010			2381+	DC A(16)
000028BC	000028E0			2382+	DC A(RE63)
				2383+*	
000028C0				2384+X63	DS OF
000028C0	E710 8F2C 0006		0000112C	2385+	VL V1, V1FUDGE
000028C6	E320 500C 0014		000028AC	2386+	LGF R2, V2_63
000028CC	E722 0000 0006		00000000	2387+	VL V2, 0(R2)
000028D2	E730 5009 7000		000028A9	2388+	VLEB V3, V3_63, 7
000028D8	E612 3010 3074			2389+	VSCHP V1, V2, V3, 3, 1
000028DE	07FB			2390+	BR R11
000028E0				2391+RE63	DS OF
000028E0				2392+	DROP R5
000028E0	50800000 00000000			2393	DC XL16' 50800000000000000000000000000000'
000028E8	00000000 00000000				

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
000028F0	00000000 00009223			2394	DC	XL16' 00000000000009223372036854775807C'
000028F8	37203685 4775807C					
				2395		
				2396 *	18446744073709551615	
				2397	VRR_B VSCHP, 3, 1, 0	
00002900				2398+	DS	OFD
00002900		00002900		2399+	USING	*, R5
00002900	00002920			2400+T64	DC	A(X64)
00002904	0040			2401+	DC	H' 64'
00002906	00			2402+	DC	X' 00'
00002907	03			2403+	DC	HL1' 3'
00002908	01			2404+	DC	HL1' 1'
00002909	00			2405+V3_64	DC	HL1' 0'
0000290C	00002950			2406+V2_64	DC	A(RE64+16)
00002910	E5E2C3C8 D7404040			2407+	DC	CL8' VSCHP'
00002918	00000010			2408+	DC	A(16)
0000291C	00002940			2409+	DC	A(RE64)
				2410+*		
00002920				2411+X64	DS	OF
00002920	E710 8F2C 0006		0000112C	2412+	VL	V1, V1FUDGE
00002926	E320 500C 0014		0000290C	2413+	LGF	R2, V2_64
0000292C	E722 0000 0006		00000000	2414+	VL	V2, 0(R2)
00002932	E730 5009 7000		00002909	2415+	VLEB	V3, V3_64, 7
00002938	E612 3010 3074			2416+	VSCHP	V1, V2, V3, 3, 1
0000293E	07FB			2417+	BR	R11
00002940				2418+RE64	DS	OF
00002940				2419+	DROP	R5
00002940	51100000 00000000			2420	DC	XL16' 51100000000000000000000000000000'
00002948	00000000 00000000					
00002950	00000000 00018446			2421	DC	XL16' 00000000000018446744073709551615C'
00002958	74407370 9551615C					
				2422		
				2423 *	90090000000018446744073709551615	
				2424	VRR_B VSCHP, 3, 1, 0	
00002960				2425+	DS	OFD
00002960		00002960		2426+	USING	*, R5
00002960	00002980			2427+T65	DC	A(X65)
00002964	0041			2428+	DC	H' 65'
00002966	00			2429+	DC	X' 00'
00002967	03			2430+	DC	HL1' 3'
00002968	01			2431+	DC	HL1' 1'
00002969	00			2432+V3_65	DC	HL1' 0'
0000296C	000029B0			2433+V2_65	DC	A(RE65+16)
00002970	E5E2C3C8 D7404040			2434+	DC	CL8' VSCHP'
00002978	00000010			2435+	DC	A(16)
0000297C	000029A0			2436+	DC	A(RE65)
				2437+*		
00002980				2438+X65	DS	OF
00002980	E710 8F2C 0006		0000112C	2439+	VL	V1, V1FUDGE
00002986	E320 500C 0014		0000296C	2440+	LGF	R2, V2_65
0000298C	E722 0000 0006		00000000	2441+	VL	V2, 0(R2)
00002992	E730 5009 7000		00002969	2442+	VLEB	V3, V3_65, 7
00002998	E612 3010 3074			2443+	VSCHP	V1, V2, V3, 3, 1
0000299E	07FB			2444+	BR	R11
000029A0				2445+RE65	DS	OF
000029A0				2446+	DROP	R5

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
000029A0	5A71B5A6 23751871			2447	DC	XL16' 5A71B5A6237518710000000000000000'
000029A8	00000000 00000000					
000029B0	90090000 00018446			2448	DC	XL16' 90090000000018446744073709551615C'
000029B8	74407370 9551615C					
				2449		
				2450 *	99999999990018446744073709551615	
				2451	VRR_B VSCHP, 3, 1, 0	
000029C0				2452+	DS	OFD
000029C0		000029C0		2453+	USING	*, R5
000029C0	000029E0			2454+T66	DC	A(X66)
000029C4	0042			2455+	DC	H' 66'
000029C6	00			2456+	DC	X' 00'
000029C7	03			2457+	DC	HL1' 3'
000029C8	01			2458+	DC	HL1' 1'
000029C9	00			2459+V3_66	DC	HL1' 0'
000029CC	00002A10			2460+V2_66	DC	A(RE66+16)
000029D0	E5E2C3C8 D7404040			2461+	DC	CL8' VSCHP'
000029D8	00000010			2462+	DC	A(16)
000029DC	00002A00			2463+	DC	A(RE66)
				2464+*		
000029E0				2465+X66	DS	OF
000029E0	E710 8F2C 0006		0000112C	2466+	VL	V1, V1FUDGE
000029E6	E320 500C 0014		000029CC	2467+	LGF	R2, V2_66
000029EC	E722 0000 0006		00000000	2468+	VL	V2, 0(R2)
000029F2	E730 5009 7000		000029C9	2469+	VLEB	V3, V3_66, 7
000029F8	E612 3010 3074			2470+	VSCHP	V1, V2, V3, 3, 1
000029FE	07FB			2471+	BR	R11
00002A00				2472+RE66	DS	OF
00002A00				2473+	DROP	R5
00002A00	5A7E37BE 1E05A6B1			2474	DC	XL16' 5A7E37BE1E05A6B10000000000000000'
00002A08	00000000 00000000					
00002A10	99999999 90018446			2475	DC	XL16' 9999999990018446744073709551615C'
00002A18	74407370 9551615C					
				2476 *	-----	
				2477 *	extended float	
				2478 *	-----	
				2479 *	+0	
				2480	VRR_B VSCHP, 4, 1, 0	
00002A20				2481+	DS	OFD
00002A20		00002A20		2482+	USING	*, R5
00002A20	00002A40			2483+T67	DC	A(X67)
00002A24	0043			2484+	DC	H' 67'
00002A26	00			2485+	DC	X' 00'
00002A27	04			2486+	DC	HL1' 4'
00002A28	01			2487+	DC	HL1' 1'
00002A29	00			2488+V3_67	DC	HL1' 0'
00002A2C	00002A70			2489+V2_67	DC	A(RE67+16)
00002A30	E5E2C3C8 D7404040			2490+	DC	CL8' VSCHP'
00002A38	00000010			2491+	DC	A(16)
00002A3C	00002A60			2492+	DC	A(RE67)
				2493+*		
00002A40				2494+X67	DS	OF
00002A40	E710 8F2C 0006		0000112C	2495+	VL	V1, V1FUDGE
00002A46	E320 500C 0014		00002A2C	2496+	LGF	R2, V2_67
00002A4C	E722 0000 0006		00000000	2497+	VL	V2, 0(R2)
00002A52	E730 5009 7000		00002A29	2498+	VLEB	V3, V3_67, 7

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002A58	E612 3010 4074			2499+	VSCHP	V1, V2, V3, 4, 1	test instruction
00002A5E	07FB			2500+	BR	R11	return
00002A60				2501+RE67	DS	0F	expected 16 byte result
00002A60				2502+	DROP	R5	
00002A60	00000000 00000000			2503	DC	XL16' 00000000000000000000000000000000'	
00002A68	00000000 00000000						
00002A70	00000000 00000000			2504	DC	XL16' 00000000000000000000000000000000C'	
00002A78	00000000 0000000C						
				2505 * -0			
00002A80				2506	VRR_B	VSCHP, 4, 1, 0	
00002A80		00002A80		2507+	DS	0FD	
00002A80	00002AA0			2508+	USING	*, R5	base for test data and test routine
00002A84	0044			2509+T68	DC	A(X68)	address of test routine
00002A86	00			2510+	DC	H' 68'	test number
00002A87	04			2511+	DC	X' 00'	
00002A88	01			2512+	DC	HL1' 4'	m4
00002A89	00			2513+	DC	HL1' 1'	m5
00002A8C	00002AD0			2514+V3_68	DC	HL1' 0'	scale
00002A90	E5E2C3C8 D7404040			2515+V2_68	DC	A(RE68+16)	address of v2: 16-byte packed decimal
00002A98	00000010			2516+	DC	CL8' VSCHP'	instruction name
00002A9C	00002AC0			2517+	DC	A(16)	result length
				2518+	DC	A(RE68)	address of expected result
				2519+*			
00002AA0				2520+X68	DS	0F	
00002AA0	E710 8F2C 0006		0000112C	2521+	VL	V1, V1FUDGE	fudge V1
00002AA6	E320 500C 0014		00002A8C	2522+	LGF	R2, V2_68	get v2
00002AAC	E722 0000 0006		00000000	2523+	VL	V2, 0(R2)	
00002AB2	E730 5009 7000		00002A89	2524+	VLEB	V3, V3_68, 7	get v3 scale
00002AB8	E612 3010 4074			2525+	VSCHP	V1, V2, V3, 4, 1	test instruction
00002ABE	07FB			2526+	BR	R11	return
00002AC0				2527+RE68	DS	0F	expected 16 byte result
00002AC0				2528+	DROP	R5	
00002AC0	00000000 00000000			2529	DC	XL16' 00000000000000000000000000000000'	
00002AC8	00000000 00000000						
00002AD0	00000000 00000000			2530	DC	XL16' 00000000000000000000000000000000D'	
00002AD8	00000000 0000000D						
				2531 * +1			
00002AE0				2532	VRR_B	VSCHP, 4, 1, 0	
00002AE0		00002AE0		2533+	DS	0FD	
00002AE0	00002B00			2534+	USING	*, R5	base for test data and test routine
00002AE4	0045			2535+T69	DC	A(X69)	address of test routine
00002AE6	00			2536+	DC	H' 69'	test number
00002AE7	04			2537+	DC	X' 00'	
00002AE8	01			2538+	DC	HL1' 4'	m4
00002AE9	00			2539+	DC	HL1' 1'	m5
00002AEC	00002B30			2540+V3_69	DC	HL1' 0'	scale
00002AF0	E5E2C3C8 D7404040			2541+V2_69	DC	A(RE69+16)	address of v2: 16-byte packed decimal
00002AF8	00000010			2542+	DC	CL8' VSCHP'	instruction name
00002AFC	00002B20			2543+	DC	A(16)	result length
				2544+	DC	A(RE69)	address of expected result
				2545+*			
00002B00				2546+X69	DS	0F	
00002B00	E710 8F2C 0006		0000112C	2547+	VL	V1, V1FUDGE	fudge V1
00002B06	E320 500C 0014		00002AEC	2548+	LGF	R2, V2_69	get v2
00002B0C	E722 0000 0006		00000000	2549+	VL	V2, 0(R2)	
00002B12	E730 5009 7000		00002AE9	2550+	VLEB	V3, V3_69, 7	get v3 scale

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00002B18	E612 3010 4074			2551+	VSCHP V1, V2, V3, 4, 1	test instruction
00002B1E	07FB			2552+	BR R11	return
00002B20				2553+RE69	DS 0F	expected 16 byte result
00002B20				2554+	DROP R5	
00002B20	41100000 00000000			2555	DC XL16' 41100000000000000330000000000000'	
00002B28	33000000 00000000					
00002B30	00000000 00000000			2556	DC XL16' 00000000000000000000000000000001C'	
00002B38	00000000 0000001C					
				2557 * -1		
				2558	VRR_B VSCHP, 4, 1, 0	
00002B40				2559+	DS 0FD	
00002B40		00002B40		2560+	USING *, R5	base for test data and test routine
00002B40	00002B60			2561+T70	DC A(X70)	address of test routine
00002B44	0046			2562+	DC H' 70'	test number
00002B46	00			2563+	DC X' 00'	
00002B47	04			2564+	DC HL1' 4'	m4
00002B48	01			2565+	DC HL1' 1'	m5
00002B49	00			2566+V3_70	DC HL1' 0'	scale
00002B4C	00002B90			2567+V2_70	DC A(RE70+16)	address of v2: 16-byte packed decimal
00002B50	E5E2C3C8 D7404040			2568+	DC CL8' VSCHP'	instruction name
00002B58	00000010			2569+	DC A(16)	result length
00002B5C	00002B80			2570+	DC A(RE70)	address of expected result
				2571+*		
00002B60				2572+X70	DS 0F	
00002B60	E710 8F2C 0006		0000112C	2573+	VL V1, V1FUDGE	fudge V1
00002B66	E320 500C 0014		00002B4C	2574+	LGF R2, V2_70	get v2
00002B6C	E722 0000 0006		00000000	2575+	VL V2, 0(R2)	
00002B72	E730 5009 7000		00002B49	2576+	VLEB V3, V3_70, 7	get v3 scale
00002B78	E612 3010 4074			2577+	VSCHP V1, V2, V3, 4, 1	test instruction
00002B7E	07FB			2578+	BR R11	return
00002B80				2579+RE70	DS 0F	expected 16 byte result
00002B80				2580+	DROP R5	
00002B80	C1100000 00000000			2581	DC XL16' C1100000000000000B30000000000000'	
00002B88	B3000000 00000000					
00002B90	00000000 00000000			2582	DC XL16' 0000000000000000000000000000001D'	
00002B98	00000000 0000001D					
				2583		
				2584 * +900000000000000001		
				2585	VRR_B VSCHP, 4, 1, 0	
00002BA0				2586+	DS 0FD	
00002BA0		00002BA0		2587+	USING *, R5	base for test data and test routine
00002BA0	00002BC0			2588+T71	DC A(X71)	address of test routine
00002BA4	0047			2589+	DC H' 71'	test number
00002BA6	00			2590+	DC X' 00'	
00002BA7	04			2591+	DC HL1' 4'	m4
00002BA8	01			2592+	DC HL1' 1'	m5
00002BA9	00			2593+V3_71	DC HL1' 0'	scale
00002BAC	00002BF0			2594+V2_71	DC A(RE71+16)	address of v2: 16-byte packed decimal
00002BB0	E5E2C3C8 D7404040			2595+	DC CL8' VSCHP'	instruction name
00002BB8	00000010			2596+	DC A(16)	result length
00002BBC	00002BE0			2597+	DC A(RE71)	address of expected result
				2598+*		
00002BC0				2599+X71	DS 0F	
00002BC0	E710 8F2C 0006		0000112C	2600+	VL V1, V1FUDGE	fudge V1
00002BC6	E320 500C 0014		00002BAC	2601+	LGF R2, V2_71	get v2
00002BCC	E722 0000 0006		00000000	2602+	VL V2, 0(R2)	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002BD2	E730 5009 7000		00002BA9	2603+	VLEB	V3, V3_71, 7	get v3 scale
00002BD8	E612 3010 4074			2604+	VSCHP	V1, V2, V3, 4, 1	test instruction
00002BDE	07FB			2605+	BR	R11	return
00002BE0				2606+RE71	DS	0F	expected 16 byte result
00002BE0				2607+	DROP	R5	
00002BE0	4E1FF973 CAFA8001			2608	DC	XL16' 4E1FF973CAFA80014000000000000000'	
00002BE8	40000000 00000000						
00002BF0	00000000 00000009			2609	DC	XL16' 00000000000000009000000000000001C'	
00002BF8	00000000 0000001C						
				2610			
				2611 *		- 9223372036854775808	
				2612	VRR_B	VSCHP, 4, 1, 0	
00002C00				2613+	DS	0FD	
00002C00		00002C00		2614+	USING	*, R5	base for test data and test routine
00002C00	00002C20			2615+T72	DC	A(X72)	address of test routine
00002C04	0048			2616+	DC	H' 72'	test number
00002C06	00			2617+	DC	X' 00'	
00002C07	04			2618+	DC	HL1' 4'	m4
00002C08	01			2619+	DC	HL1' 1'	m5
00002C09	00			2620+V3_72	DC	HL1' 0'	scale
00002C0C	00002C50			2621+V2_72	DC	A(RE72+16)	address of v2: 16-byte packed decimal
00002C10	E5E2C3C8 D7404040			2622+	DC	CL8' VSCHP'	instruction name
00002C18	00000010			2623+	DC	A(16)	result length
00002C1C	00002C40			2624+	DC	A(RE72)	address of expected result
				2625+*			
00002C20				2626+X72	DS	0F	
00002C20	E710 8F2C 0006		0000112C	2627+	VL	V1, V1FUDGE	fudge V1
00002C26	E320 500C 0014		00002C0C	2628+	LGF	R2, V2_72	get v2
00002C2C	E722 0000 0006		00000000	2629+	VL	V2, 0(R2)	
00002C32	E730 5009 7000		00002C09	2630+	VLEB	V3, V3_72, 7	get v3 scale
00002C38	E612 3010 4074			2631+	VSCHP	V1, V2, V3, 4, 1	test instruction
00002C3E	07FB			2632+	BR	R11	return
00002C40				2633+RE72	DS	0F	expected 16 byte result
00002C40				2634+	DROP	R5	
00002C40	D0800000 00000000			2635	DC	XL16' D080000000000000C200000000000000'	
00002C48	C2000000 00000000						
00002C50	00000000 00009223			2636	DC	XL16' 00000000000009223372036854775808D'	
00002C58	37203685 4775808D						
				2637			
				2638 *		9223372036854775807	
				2639	VRR_B	VSCHP, 4, 1, 0	
00002C60				2640+	DS	0FD	
00002C60		00002C60		2641+	USING	*, R5	base for test data and test routine
00002C60	00002C80			2642+T73	DC	A(X73)	address of test routine
00002C64	0049			2643+	DC	H' 73'	test number
00002C66	00			2644+	DC	X' 00'	
00002C67	04			2645+	DC	HL1' 4'	m4
00002C68	01			2646+	DC	HL1' 1'	m5
00002C69	00			2647+V3_73	DC	HL1' 0'	scale
00002C6C	00002CB0			2648+V2_73	DC	A(RE73+16)	address of v2: 16-byte packed decimal
00002C70	E5E2C3C8 D7404040			2649+	DC	CL8' VSCHP'	instruction name
00002C78	00000010			2650+	DC	A(16)	result length
00002C7C	00002CA0			2651+	DC	A(RE73)	address of expected result
				2652+*			
00002C80				2653+X73	DS	0F	
00002C80	E710 8F2C 0006		0000112C	2654+	VL	V1, V1FUDGE	fudge V1

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002C86	E320 500C 0014		00002C6C	2655+	LGF	R2, V2_73	get v2
00002C8C	E722 0000 0006		00000000	2656+	VL	V2, 0(R2)	
00002C92	E730 5009 7000		00002C69	2657+	VLEB	V3, V3_73, 7	get v3 scale
00002C98	E612 3010 4074			2658+	VSCHP	V1, V2, V3, 4, 1	test instruction
00002C9E	07FB			2659+	BR	R11	return
00002CA0				2660+RE73	DS	0F	expected 16 byte result
00002CA0				2661+	DROP	R5	
00002CA0	507FFFFFFF FFFFFFFF			2662	DC	XL16' 507FFFFFFF42FF000000000000'	
00002CA8	42FF0000 00000000						
00002CB0	00000000 00009223			2663	DC	XL16' 0000000000009223372036854775807C'	
00002CB8	37203685 4775807C						
				2664			
				2665 *	18446744073709551615		
				2666	VRR_B VSCHP, 4, 1, 0		
00002CC0				2667+	DS	0FD	
00002CC0		00002CC0		2668+	USING	*, R5	base for test data and test routine
00002CC0	00002CE0			2669+T74	DC	A(X74)	address of test routine
00002CC4	004A			2670+	DC	H' 74'	test number
00002CC6	00			2671+	DC	X' 00'	
00002CC7	04			2672+	DC	HL1' 4'	m4
00002CC8	01			2673+	DC	HL1' 1'	m5
00002CC9	00			2674+V3_74	DC	HL1' 0'	scale
00002CCC	00002D10			2675+V2_74	DC	A(RE74+16)	address of v2: 16-byte packed decimal
00002CD0	E5E2C3C8 D7404040			2676+	DC	CL8' VSCHP'	instruction name
00002CD8	00000010			2677+	DC	A(16)	result length
00002CDC	00002D00			2678+	DC	A(RE74)	address of expected result
				2679+*			
00002CE0				2680+X74	DS	0F	
00002CE0	E710 8F2C 0006		0000112C	2681+	VL	V1, V1FUDGE	fudge V1
00002CE6	E320 500C 0014		00002CCC	2682+	LGF	R2, V2_74	get v2
00002CEC	E722 0000 0006		00000000	2683+	VL	V2, 0(R2)	
00002CF2	E730 5009 7000		00002CC9	2684+	VLEB	V3, V3_74, 7	get v3 scale
00002CF8	E612 3010 4074			2685+	VSCHP	V1, V2, V3, 4, 1	test instruction
00002CFE	07FB			2686+	BR	R11	return
00002D00				2687+RE74	DS	0F	expected 16 byte result
00002D00				2688+	DROP	R5	
00002D00	50FFFFFF FFFFFFFF			2689	DC	XL16' 50FFFFFF42FF000000000000'	
00002D08	42FF0000 00000000						
00002D10	00000000 00018446			2690	DC	XL16' 0000000000018446744073709551615C'	
00002D18	74407370 9551615C						
				2691			
				2692 *	9009000000018446744073709551615		
				2693	VRR_B VSCHP, 4, 1, 0		
00002D20				2694+	DS	0FD	
00002D20		00002D20		2695+	USING	*, R5	base for test data and test routine
00002D20	00002D40			2696+T75	DC	A(X75)	address of test routine
00002D24	004B			2697+	DC	H' 75'	test number
00002D26	00			2698+	DC	X' 00'	
00002D27	04			2699+	DC	HL1' 4'	m4
00002D28	01			2700+	DC	HL1' 1'	m5
00002D29	00			2701+V3_75	DC	HL1' 0'	scale
00002D2C	00002D70			2702+V2_75	DC	A(RE75+16)	address of v2: 16-byte packed decimal
00002D30	E5E2C3C8 D7404040			2703+	DC	CL8' VSCHP'	instruction name
00002D38	00000010			2704+	DC	A(16)	result length
00002D3C	00002D60			2705+	DC	A(RE75)	address of expected result
				2706+*			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002D40				2707+X75	DS	0F	
00002D40	E710 8F2C 0006		0000112C	2708+	VL	V1, V1FUDGE	fudge V1
00002D46	E320 500C 0014		00002D2C	2709+	LGF	R2, V2_75	get v2
00002D4C	E722 0000 0006		00000000	2710+	VL	V2, 0(R2)	
00002D52	E730 5009 7000		00002D29	2711+	VLEB	V3, V3_75, 7	get v3 scale
00002D58	E612 3010 4074			2712+	VSCHP	V1, V2, V3, 4, 1	test instruction
00002D5E	07FB			2713+	BR	R11	return
00002D60				2714+RE75	DS	0F	expected 16 byte result
00002D60				2715+	DROP	R5	
00002D60	5A71B5A6 23751870			2716	DC	XL16' 5A71B5A6237518704CDF6067FFFFFFF00'	
00002D68	4CDF6067 FFFFFFF00						
00002D70	90090000 00018446			2717	DC	XL16' 90090000000018446744073709551615C'	
00002D78	74407370 9551615C						
				2718			
				2719	*	99999999990018446744073709551615	
				2720	VRR_B	VSCHP, 4, 1, 0	
00002D80				2721+	DS	0FD	
00002D80		00002D80		2722+	USING	*, R5	base for test data and test routine
00002D80	00002DA0			2723+T76	DC	A(X76)	address of test routine
00002D84	004C			2724+	DC	H' 76'	test number
00002D86	00			2725+	DC	X' 00'	
00002D87	04			2726+	DC	HL1' 4'	m4
00002D88	01			2727+	DC	HL1' 1'	m5
00002D89	00			2728+V3_76	DC	HL1' 0'	scale
00002D8C	00002DD0			2729+V2_76	DC	A(RE76+16)	address of v2: 16-byte packed decimal
00002D90	E5E2C3C8 D7404040			2730+	DC	CL8' VSCHP'	instruction name
00002D98	00000010			2731+	DC	A(16)	result length
00002D9C	00002DC0			2732+	DC	A(RE76)	address of expected result
				2733+*			
00002DA0				2734+X76	DS	0F	
00002DA0	E710 8F2C 0006		0000112C	2735+	VL	V1, V1FUDGE	fudge V1
00002DA6	E320 500C 0014		00002D8C	2736+	LGF	R2, V2_76	get v2
00002DAC	E722 0000 0006		00000000	2737+	VL	V2, 0(R2)	
00002DB2	E730 5009 7000		00002D89	2738+	VLEB	V3, V3_76, 7	get v3 scale
00002DB8	E612 3010 4074			2739+	VSCHP	V1, V2, V3, 4, 1	test instruction
00002DBE	07FB			2740+	BR	R11	return
00002DC0				2741+RE76	DS	0F	expected 16 byte result
00002DC0				2742+	DROP	R5	
00002DC0	5A7E37BE 1E05A6B0			2743	DC	XL16' 5A7E37BE1E05A6B04C816BCDBFFFFFFF00'	
00002DC8	4C816BCD BFFFFFF00						
00002DD0	99999999 90018446			2744	DC	XL16' 99999999990018446744073709551615C'	
00002DD8	74407370 9551615C						
				2745			
				2746			
				2747	*	-----	
				2748	*	ROUND - with shifts	
				2749	*	-----	
				2750	*	-----	
				2751	*	short float	
				2752	*	-----	
				2753	*	+0	
				2754	VRR_B	VSCHP, 2, 1, 1	
00002DE0				2755+	DS	0FD	
00002DE0		00002DE0		2756+	USING	*, R5	base for test data and test routine
00002DE0	00002E00			2757+T77	DC	A(X77)	address of test routine
00002DE4	004D			2758+	DC	H' 77'	test number

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00002DE6	00			2759+	DC	X' 00'
00002DE7	02			2760+	DC	HL1' 2' m4
00002DE8	01			2761+	DC	HL1' 1' m5
00002DE9	01			2762+V3_77	DC	HL1' 1' scale
00002DEC	00002E30			2763+V2_77	DC	A(RE77+16) address of v2: 16-byte packed decimal
00002DF0	E5E2C3C8 D7404040			2764+	DC	CL8' VSCHP' instruction name
00002DF8	00000010			2765+	DC	A(16) result length
00002DFC	00002E20			2766+	DC	A(RE77) address of expected result
				2767+*		
00002E00				2768+X77	DS	0F
00002E00	E710 8F2C 0006		0000112C	2769+	VL	V1, V1FUDGE fudge V1
00002E06	E320 500C 0014		00002DEC	2770+	LGF	R2, V2_77 get v2
00002E0C	E722 0000 0006		00000000	2771+	VL	V2, 0(R2)
00002E12	E730 5009 7000		00002DE9	2772+	VLEB	V3, V3_77, 7 get v3 scale
00002E18	E612 3010 2074			2773+	VSCHP	V1, V2, V3, 2, 1 test instruction
00002E1E	07FB			2774+	BR	R11 return
00002E20				2775+RE77	DS	0F expected 16 byte result
00002E20				2776+	DROP	R5
00002E20	00000000 00000000			2777	DC	XL16' 00000000000000000000000000000000'
00002E28	00000000 00000000					
00002E30	00000000 00000000			2778	DC	XL16' 00000000000000000000000000000000C'
00002E38	00000000 0000000C					
				2779 * -0		
00002E40				2780	VRR_B	VSCHP, 2, 1, 1
00002E40		00002E40		2781+	DS	0FD
00002E40	00002E60			2782+	USING	*, R5 base for test data and test routine
00002E44	004E			2783+T78	DC	A(X78) address of test routine
00002E46	00			2784+	DC	H' 78' test number
00002E47	02			2785+	DC	X' 00'
00002E48	01			2786+	DC	HL1' 2' m4
00002E49	01			2787+	DC	HL1' 1' m5
00002E4C	00002E90			2788+V3_78	DC	HL1' 1' scale
00002E50	E5E2C3C8 D7404040			2789+V2_78	DC	A(RE78+16) address of v2: 16-byte packed decimal
00002E58	00000010			2790+	DC	CL8' VSCHP' instruction name
00002E5C	00002E80			2791+	DC	A(16) result length
				2792+	DC	A(RE78) address of expected result
				2793+*		
00002E60				2794+X78	DS	0F
00002E60	E710 8F2C 0006		0000112C	2795+	VL	V1, V1FUDGE fudge V1
00002E66	E320 500C 0014		00002E4C	2796+	LGF	R2, V2_78 get v2
00002E6C	E722 0000 0006		00000000	2797+	VL	V2, 0(R2)
00002E72	E730 5009 7000		00002E49	2798+	VLEB	V3, V3_78, 7 get v3 scale
00002E78	E612 3010 2074			2799+	VSCHP	V1, V2, V3, 2, 1 test instruction
00002E7E	07FB			2800+	BR	R11 return
00002E80				2801+RE78	DS	0F expected 16 byte result
00002E80				2802+	DROP	R5
00002E80	00000000 00000000			2803	DC	XL16' 00000000000000000000000000000000'
00002E88	00000000 00000000					
00002E90	00000000 00000000			2804	DC	XL16' 00000000000000000000000000000000D'
00002E98	00000000 0000000D					
				2805 * +1		
00002EA0				2806	VRR_B	VSCHP, 2, 1, 1
00002EA0		00002EA0		2807+	DS	0FD
00002EA0	00002EC0			2808+	USING	*, R5 base for test data and test routine
00002EA4	004F			2809+T79	DC	A(X79) address of test routine
				2810+	DC	H' 79' test number





LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00003020		00003020		2915+	USING *,R5	base for test data and test routine
00003020	00003040			2916+T83	DC A(X83)	address of test routine
00003024	0053			2917+	DC H' 83'	test number
00003026	00			2918+	DC X' 00'	
00003027	02			2919+	DC HL1' 2'	m4
00003028	01			2920+	DC HL1' 1'	m5
00003029	02			2921+V3_83	DC HL1' 2'	scale
0000302C	00003070			2922+V2_83	DC A(RE83+16)	address of v2: 16-byte packed decimal
00003030	E5E2C3C8 D7404040			2923+	DC CL8' VSCHP'	instruction name
00003038	00000010			2924+	DC A(16)	result length
0000303C	00003060			2925+	DC A(RE83)	address of expected result
				2926+*		
00003040				2927+X83	DS 0F	
00003040	E710 8F2C 0006		0000112C	2928+	VL V1, V1FUDGE	fudge V1
00003046	E320 500C 0014		0000302C	2929+	LGF R2, V2_83	get v2
0000304C	E722 0000 0006		00000000	2930+	VL V2, 0(R2)	
00003052	E730 5009 7000		00003029	2931+	VLEB V3, V3_83, 7	get v3 scale
00003058	E612 3010 2074			2932+	VSCHP V1, V2, V3, 2, 1	test instruction
0000305E	07FB			2933+	BR R11	return
00003060				2934+RE83	DS 0F	expected 16 byte result
00003060				2935+	DROP R5	
00003060	52320000 00000000			2936	DC XL16' 52320000000000000000000000000000'	
00003068	00000000 00000000					
00003070	00000000 00009223			2937	DC XL16' 00000000000009223372036854775807C'	
00003078	37203685 4775807C					
				2938		
				2939 *	18446744073709551615	
				2940	VRR_B VSCHP, 2, 1, 2	
00003080				2941+	DS 0FD	
00003080		00003080		2942+	USING *,R5	base for test data and test routine
00003080	000030A0			2943+T84	DC A(X84)	address of test routine
00003084	0054			2944+	DC H' 84'	test number
00003086	00			2945+	DC X' 00'	
00003087	02			2946+	DC HL1' 2'	m4
00003088	01			2947+	DC HL1' 1'	m5
00003089	02			2948+V3_84	DC HL1' 2'	scale
0000308C	000030D0			2949+V2_84	DC A(RE84+16)	address of v2: 16-byte packed decimal
00003090	E5E2C3C8 D7404040			2950+	DC CL8' VSCHP'	instruction name
00003098	00000010			2951+	DC A(16)	result length
0000309C	000030C0			2952+	DC A(RE84)	address of expected result
				2953+*		
000030A0				2954+X84	DS 0F	
000030A0	E710 8F2C 0006		0000112C	2955+	VL V1, V1FUDGE	fudge V1
000030A6	E320 500C 0014		0000308C	2956+	LGF R2, V2_84	get v2
000030AC	E722 0000 0006		00000000	2957+	VL V2, 0(R2)	
000030B2	E730 5009 7000		00003089	2958+	VLEB V3, V3_84, 7	get v3 scale
000030B8	E612 3010 2074			2959+	VSCHP V1, V2, V3, 2, 1	test instruction
000030BE	07FB			2960+	BR R11	return
000030C0				2961+RE84	DS 0F	expected 16 byte result
000030C0				2962+	DROP R5	
000030C0	52640000 00000000			2963	DC XL16' 52640000000000000000000000000000'	
000030C8	00000000 00000000					
000030D0	00000000 00018446			2964	DC XL16' 00000000000018446744073709551615C'	
000030D8	74407370 9551615C					
				2965		
				2966 *	-----	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				2967 * long float	
				2968 * -----	
				2969 * +0	
000030E0				2970 VRR_B VSCHP, 3, 1, 1	
000030E0		000030E0		2971+ DS OFD	
000030E0	00003100			2972+ USING *, R5	base for test data and test routine
000030E4	0055			2973+T85 DC A(X85)	address of test routine
000030E6	00			2974+ DC H' 85'	test number
000030E7	03			2975+ DC X' 00'	
000030E8	01			2976+ DC HL1' 3'	m4
000030E9	01			2977+ DC HL1' 1'	m5
000030EC	00003130			2978+V3_85 DC HL1' 1'	scale
000030F0	E5E2C3C8 D7404040			2979+V2_85 DC A(RE85+16)	address of v2: 16-byte packed decimal
000030F8	00000010			2980+ DC CL8' VSCHP'	instruction name
000030FC	00003120			2981+ DC A(16)	result length
				2982+ DC A(RE85)	address of expected result
				2983+*	
00003100				2984+X85 DS OF	
00003100	E710 8F2C 0006	0000112C		2985+ VL V1, V1FUDGE	fudge V1
00003106	E320 500C 0014	000030EC		2986+ LGF R2, V2_85	get v2
0000310C	E722 0000 0006	00000000		2987+ VL V2, 0(R2)	
00003112	E730 5009 7000	000030E9		2988+ VLEB V3, V3_85, 7	get v3 scale
00003118	E612 3010 3074			2989+ VSCHP V1, V2, V3, 3, 1	test instruction
0000311E	07FB			2990+ BR R11	return
00003120				2991+RE85 DS OF	expected 16 byte result
00003120				2992+ DROP R5	
00003120	00000000 00000000			2993 DC XL16' 00000000000000000000000000000000'	
00003128	00000000 00000000				
00003130	00000000 00000000			2994 DC XL16' 00000000000000000000000000000000C'	
00003138	00000000 0000000C				
				2995 * - 0	
				2996 VRR_B VSCHP, 3, 1, 1	
00003140		00003140		2997+ DS OFD	
00003140				2998+ USING *, R5	base for test data and test routine
00003140	00003160			2999+T86 DC A(X86)	address of test routine
00003144	0056			3000+ DC H' 86'	test number
00003146	00			3001+ DC X' 00'	
00003147	03			3002+ DC HL1' 3'	m4
00003148	01			3003+ DC HL1' 1'	m5
00003149	01			3004+V3_86 DC HL1' 1'	scale
0000314C	00003190			3005+V2_86 DC A(RE86+16)	address of v2: 16-byte packed decimal
00003150	E5E2C3C8 D7404040			3006+ DC CL8' VSCHP'	instruction name
00003158	00000010			3007+ DC A(16)	result length
0000315C	00003180			3008+ DC A(RE86)	address of expected result
				3009+*	
00003160				3010+X86 DS OF	
00003160	E710 8F2C 0006	0000112C		3011+ VL V1, V1FUDGE	fudge V1
00003166	E320 500C 0014	0000314C		3012+ LGF R2, V2_86	get v2
0000316C	E722 0000 0006	00000000		3013+ VL V2, 0(R2)	
00003172	E730 5009 7000	00003149		3014+ VLEB V3, V3_86, 7	get v3 scale
00003178	E612 3010 3074			3015+ VSCHP V1, V2, V3, 3, 1	test instruction
0000317E	07FB			3016+ BR R11	return
00003180				3017+RE86 DS OF	expected 16 byte result
00003180				3018+ DROP R5	
00003180	00000000 00000000			3019 DC XL16' 00000000000000000000000000000000'	
00003188	00000000 00000000				

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00003190	00000000 00000000			3020	DC	XL16' 000000000000000000000000000000D'
00003198	00000000 0000000D					
				3021 * +1		
000031A0				3022	VRR_B	VSCHP, 3, 1, 1
000031A0		000031A0		3023+	DS	OFD
000031A0	000031C0			3024+	USING	*, R5
000031A4	0057			3025+T87	DC	A(X87)
000031A6	00			3026+	DC	H' 87'
000031A7	03			3027+	DC	X' 00'
000031A8	01			3028+	DC	HL1' 3'
000031A9	01			3029+	DC	HL1' 1'
000031AC	000031F0			3030+V3_87	DC	HL1' 1'
000031B0	E5E2C3C8 D7404040			3031+V2_87	DC	A(RE87+16)
000031B8	00000010			3032+	DC	CL8' VSCHP'
000031BC	000031E0			3033+	DC	A(16)
				3034+	DC	A(RE87)
				3035+*		
000031C0				3036+X87	DS	OF
000031C0	E710 8F2C 0006		0000112C	3037+	VL	V1, V1FUDGE
000031C6	E320 500C 0014		000031AC	3038+	LGF	R2, V2_87
000031CC	E722 0000 0006		00000000	3039+	VL	V2, 0(R2)
000031D2	E730 5009 7000		000031A9	3040+	VLEB	V3, V3_87, 7
000031D8	E612 3010 3074			3041+	VSCHP	V1, V2, V3, 3, 1
000031DE	07FB			3042+	BR	R11
000031E0				3043+RE87	DS	OF
000031E0				3044+	DROP	R5
000031E0	41A00000 00000000			3045	DC	XL16' 41A0000000000000000000000000000'
000031E8	00000000 00000000					
000031F0	00000000 00000000			3046	DC	XL16' 0000000000000000000000000000001C'
000031F8	00000000 0000001C					
				3047 * -1		
				3048	VRR_B	VSCHP, 3, 1, 1
00003200				3049+	DS	OFD
00003200		00003200		3050+	USING	*, R5
00003200	00003220			3051+T88	DC	A(X88)
00003204	0058			3052+	DC	H' 88'
00003206	00			3053+	DC	X' 00'
00003207	03			3054+	DC	HL1' 3'
00003208	01			3055+	DC	HL1' 1'
00003209	01			3056+V3_88	DC	HL1' 1'
0000320C	00003250			3057+V2_88	DC	A(RE88+16)
00003210	E5E2C3C8 D7404040			3058+	DC	CL8' VSCHP'
00003218	00000010			3059+	DC	A(16)
0000321C	00003240			3060+	DC	A(RE88)
				3061+*		
00003220				3062+X88	DS	OF
00003220	E710 8F2C 0006		0000112C	3063+	VL	V1, V1FUDGE
00003226	E320 500C 0014		0000320C	3064+	LGF	R2, V2_88
0000322C	E722 0000 0006		00000000	3065+	VL	V2, 0(R2)
00003232	E730 5009 7000		00003209	3066+	VLEB	V3, V3_88, 7
00003238	E612 3010 3074			3067+	VSCHP	V1, V2, V3, 3, 1
0000323E	07FB			3068+	BR	R11
00003240				3069+RE88	DS	OF
00003240				3070+	DROP	R5
00003240	C1A00000 00000000			3071	DC	XL16' C1A0000000000000000000000000000'
00003248	00000000 00000000					



LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00003300	D2320000 00000000			3125	DC	XL16' D2320000000000000000000000000000'
00003308	00000000 00000000					
00003310	00000000 00009223			3126	DC	XL16' 00000000000009223372036854775808D'
00003318	37203685 4775808D					
				3127		
				3128 *		9223372036854775807
				3129	VRR_B	VSCHP, 3, 1, 2
00003320				3130+	DS	OFD
00003320		00003320		3131+	USING	*, R5
00003320	00003340			3132+T91	DC	A(X91)
00003324	005B			3133+	DC	H' 91'
00003326	00			3134+	DC	X' 00'
00003327	03			3135+	DC	HL1' 3'
00003328	01			3136+	DC	HL1' 1'
00003329	02			3137+V3_91	DC	HL1' 2'
0000332C	00003370			3138+V2_91	DC	A(RE91+16)
00003330	E5E2C3C8 D7404040			3139+	DC	CL8' VSCHP'
00003338	00000010			3140+	DC	A(16)
0000333C	00003360			3141+	DC	A(RE91)
				3142+*		
00003340				3143+X91	DS	OF
00003340	E710 8F2C 0006		0000112C	3144+	VL	V1, V1FUDGE
00003346	E320 500C 0014		0000332C	3145+	LGF	R2, V2_91
0000334C	E722 0000 0006		00000000	3146+	VL	V2, 0(R2)
00003352	E730 5009 7000		00003329	3147+	VLEB	V3, V3_91, 7
00003358	E612 3010 3074			3148+	VSCHP	V1, V2, V3, 3, 1
0000335E	07FB			3149+	BR	R11
00003360				3150+RE91	DS	OF
00003360				3151+	DROP	R5
00003360	52320000 00000000			3152	DC	XL16' 52320000000000000000000000000000'
00003368	00000000 00000000					
00003370	00000000 00009223			3153	DC	XL16' 00000000000009223372036854775807C'
00003378	37203685 4775807C					
				3154		
				3155 *		18446744073709551615
				3156	VRR_B	VSCHP, 3, 1, 2
00003380				3157+	DS	OFD
00003380		00003380		3158+	USING	*, R5
00003380	000033A0			3159+T92	DC	A(X92)
00003384	005C			3160+	DC	H' 92'
00003386	00			3161+	DC	X' 00'
00003387	03			3162+	DC	HL1' 3'
00003388	01			3163+	DC	HL1' 1'
00003389	02			3164+V3_92	DC	HL1' 2'
0000338C	000033D0			3165+V2_92	DC	A(RE92+16)
00003390	E5E2C3C8 D7404040			3166+	DC	CL8' VSCHP'
00003398	00000010			3167+	DC	A(16)
0000339C	000033C0			3168+	DC	A(RE92)
				3169+*		
000033A0				3170+X92	DS	OF
000033A0	E710 8F2C 0006		0000112C	3171+	VL	V1, V1FUDGE
000033A6	E320 500C 0014		0000338C	3172+	LGF	R2, V2_92
000033AC	E722 0000 0006		00000000	3173+	VL	V2, 0(R2)
000033B2	E730 5009 7000		00003389	3174+	VLEB	V3, V3_92, 7
000033B8	E612 3010 3074			3175+	VSCHP	V1, V2, V3, 3, 1
000033BE	07FB			3176+	BR	R11

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
000033C0				3177+RE92	DS	OF expected 16 byte result
000033C0				3178+	DROP	R5
000033C0	52640000 00000000			3179	DC	XL16' 52640000000000000000000000000000'
000033C8	00000000 00000000					
000033D0	00000000 00018446			3180	DC	XL16' 00000000000018446744073709551615C'
000033D8	74407370 9551615C					
				3181		
				3182 *	90090000000018446744073709551615	
				3183	VRR_B	VSCHP, 3, 1, 3
000033E0				3184+	DS	OFD
000033E0		000033E0		3185+	USING	*, R5 base for test data and test routine
000033E0	00003400			3186+T93	DC	A(X93) address of test routine
000033E4	005D			3187+	DC	H' 93' test number
000033E6	00			3188+	DC	X' 00'
000033E7	03			3189+	DC	HL1' 3' m4
000033E8	01			3190+	DC	HL1' 1' m5
000033E9	03			3191+V3_93	DC	HL1' 3' scale
000033EC	00003430			3192+V2_93	DC	A(RE93+16) address of v2: 16-byte packed decimal
000033F0	E5E2C3C8 D7404040			3193+	DC	CL8' VSCHP' instruction name
000033F8	00000010			3194+	DC	A(16) result length
000033FC	00003420			3195+	DC	A(RE93) address of expected result
				3196+*		
00003400				3197+X93	DS	OF
00003400	E710 8F2C 0006		0000112C	3198+	VL	V1, V1FUDGE fudge V1
00003406	E320 500C 0014		000033EC	3199+	LGF	R2, V2_93 get v2
0000340C	E722 0000 0006		00000000	3200+	VL	V2, 0(R2)
00003412	E730 5009 7000		000033E9	3201+	VLEB	V3, V3_93, 7 get v3 scale
00003418	E612 3010 3074			3202+	VSCHP	V1, V2, V3, 3, 1 test instruction
0000341E	07FB			3203+	BR	R11 return
00003420				3204+RE93	DS	OF expected 16 byte result
00003420				3205+	DROP	R5
00003420	5D1BC2D9 0FA81678			3206	DC	XL16' 5D1BC2D90FA816780000000000000000'
00003428	00000000 00000000					
00003430	90090000 00018446			3207	DC	XL16' 90090000000018446744073709551615C'
00003438	74407370 9551615C					
				3208		
				3209 *	99999999990018446744073709551615	
				3210	VRR_B	VSCHP, 3, 1, 3
00003440				3211+	DS	OFD
00003440		00003440		3212+	USING	*, R5 base for test data and test routine
00003440	00003460			3213+T94	DC	A(X94) address of test routine
00003444	005E			3214+	DC	H' 94' test number
00003446	00			3215+	DC	X' 00'
00003447	03			3216+	DC	HL1' 3' m4
00003448	01			3217+	DC	HL1' 1' m5
00003449	03			3218+V3_94	DC	HL1' 3' scale
0000344C	00003490			3219+V2_94	DC	A(RE94+16) address of v2: 16-byte packed decimal
00003450	E5E2C3C8 D7404040			3220+	DC	CL8' VSCHP' instruction name
00003458	00000010			3221+	DC	A(16) result length
0000345C	00003480			3222+	DC	A(RE94) address of expected result
				3223+*		
00003460				3224+X94	DS	OF
00003460	E710 8F2C 0006		0000112C	3225+	VL	V1, V1FUDGE fudge V1
00003466	E320 500C 0014		0000344C	3226+	LGF	R2, V2_94 get v2
0000346C	E722 0000 0006		00000000	3227+	VL	V2, 0(R2)
00003472	E730 5009 7000		00003449	3228+	VLEB	V3, V3_94, 7 get v3 scale

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00003478	E612 3010 3074			3229+	VSCHP V1, V2, V3, 3, 1	test instruction
0000347E	07FB			3230+	BR R11	return
00003480				3231+RE94	DS 0F	expected 16 byte result
00003480				3232+	DROP R5	
00003480	5D1ED09B EA546132			3233	DC XL16' 5D1ED09BEA5461320000000000000000'	
00003488	00000000 00000000					
00003490	99999999 90018446			3234	DC XL16' 9999999990018446744073709551615C'	
00003498	74407370 9551615C					
				3235 *		
				3236 *	extended float	
				3237 *		
				3238 *	+0	
				3239	VRR_B VSCHP, 4, 1, 1	
000034A0				3240+	DS OFD	
000034A0		000034A0		3241+	USING *, R5	base for test data and test routine
000034A0	000034C0			3242+T95	DC A(X95)	address of test routine
000034A4	005F			3243+	DC H' 95'	test number
000034A6	00			3244+	DC X' 00'	
000034A7	04			3245+	DC HL1' 4'	m4
000034A8	01			3246+	DC HL1' 1'	m5
000034A9	01			3247+V3_95	DC HL1' 1'	scale
000034AC	000034F0			3248+V2_95	DC A(RE95+16)	address of v2: 16-byte packed decimal
000034B0	E5E2C3C8 D7404040			3249+	DC CL8' VSCHP'	instruction name
000034B8	00000010			3250+	DC A(16)	result length
000034BC	000034E0			3251+	DC A(RE95)	address of expected result
				3252+*		
000034C0				3253+X95	DS 0F	
000034C0	E710 8F2C 0006		0000112C	3254+	VL V1, V1FUDGE	fudge V1
000034C6	E320 500C 0014		000034AC	3255+	LGF R2, V2_95	get v2
000034CC	E722 0000 0006		00000000	3256+	VL V2, 0(R2)	
000034D2	E730 5009 7000		000034A9	3257+	VLEB V3, V3_95, 7	get v3 scale
000034D8	E612 3010 4074			3258+	VSCHP V1, V2, V3, 4, 1	test instruction
000034DE	07FB			3259+	BR R11	return
000034E0				3260+RE95	DS 0F	expected 16 byte result
000034E0				3261+	DROP R5	
000034E0	00000000 00000000			3262	DC XL16' 00000000000000000000000000000000'	
000034E8	00000000 00000000					
000034F0	00000000 00000000			3263	DC XL16' 00000000000000000000000000000000C'	
000034F8	00000000 0000000C					
				3264 *	- 0	
				3265	VRR_B VSCHP, 4, 1, 1	
00003500				3266+	DS OFD	
00003500		00003500		3267+	USING *, R5	base for test data and test routine
00003500	00003520			3268+T96	DC A(X96)	address of test routine
00003504	0060			3269+	DC H' 96'	test number
00003506	00			3270+	DC X' 00'	
00003507	04			3271+	DC HL1' 4'	m4
00003508	01			3272+	DC HL1' 1'	m5
00003509	01			3273+V3_96	DC HL1' 1'	scale
0000350C	00003550			3274+V2_96	DC A(RE96+16)	address of v2: 16-byte packed decimal
00003510	E5E2C3C8 D7404040			3275+	DC CL8' VSCHP'	instruction name
00003518	00000010			3276+	DC A(16)	result length
0000351C	00003540			3277+	DC A(RE96)	address of expected result
				3278+*		
00003520				3279+X96	DS 0F	
00003520	E710 8F2C 0006		0000112C	3280+	VL V1, V1FUDGE	fudge V1

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00003526	E320 500C 0014		0000350C	3281+	LGF	R2, V2_96	get v2
0000352C	E722 0000 0006		00000000	3282+	VL	V2, 0(R2)	
00003532	E730 5009 7000		00003509	3283+	VLEB	V3, V3_96, 7	get v3 scale
00003538	E612 3010 4074			3284+	VSCHP	V1, V2, V3, 4, 1	test instruction
0000353E	07FB			3285+	BR	R11	return
00003540				3286+RE96	DS	0F	expected 16 byte result
00003540				3287+	DROP	R5	
00003540	00000000 00000000			3288	DC	XL16' 00000000000000000000000000000000'	
00003548	00000000 00000000						
00003550	00000000 00000000			3289	DC	XL16' 00000000000000000000000000000000D'	
00003558	00000000 0000000D						
				3290 * +1			
00003560				3291	VRR_B	VSCHP, 4, 1, 1	
00003560		00003560		3292+	DS	0FD	
00003560	00003580			3293+	USING	*, R5	base for test data and test routine
00003564	0061			3294+T97	DC	A(X97)	address of test routine
00003566	00			3295+	DC	H' 97'	test number
00003567	04			3296+	DC	X' 00'	
00003568	01			3297+	DC	HL1' 4'	m4
00003569	01			3298+	DC	HL1' 1'	m5
0000356C	000035B0			3299+V3_97	DC	HL1' 1'	scale
00003570	E5E2C3C8 D7404040			3300+V2_97	DC	A(RE97+16)	address of v2: 16-byte packed decimal
00003578	00000010			3301+	DC	CL8' VSCHP'	instruction name
0000357C	000035A0			3302+	DC	A(16)	result length
				3303+	DC	A(RE97)	address of expected result
				3304+*			
00003580				3305+X97	DS	0F	
00003580	E710 8F2C 0006		0000112C	3306+	VL	V1, V1FUDGE	fudge V1
00003586	E320 500C 0014		0000356C	3307+	LGF	R2, V2_97	get v2
0000358C	E722 0000 0006		00000000	3308+	VL	V2, 0(R2)	
00003592	E730 5009 7000		00003569	3309+	VLEB	V3, V3_97, 7	get v3 scale
00003598	E612 3010 4074			3310+	VSCHP	V1, V2, V3, 4, 1	test instruction
0000359E	07FB			3311+	BR	R11	return
000035A0				3312+RE97	DS	0F	expected 16 byte result
000035A0				3313+	DROP	R5	
000035A0	41A00000 00000000			3314	DC	XL16' 41A0000000000000003300000000000000'	
000035A8	33000000 00000000						
000035B0	00000000 00000000			3315	DC	XL16' 000000000000000000000000000000001C'	
000035B8	00000000 0000001C						
				3316 * -1			
000035C0				3317	VRR_B	VSCHP, 4, 1, 1	
000035C0		000035C0		3318+	DS	0FD	
000035C0	000035E0			3319+	USING	*, R5	base for test data and test routine
000035C4	0062			3320+T98	DC	A(X98)	address of test routine
000035C6	00			3321+	DC	H' 98'	test number
000035C7	04			3322+	DC	X' 00'	
000035C8	01			3323+	DC	HL1' 4'	m4
000035C9	01			3324+	DC	HL1' 1'	m5
000035CC	00003610			3325+V3_98	DC	HL1' 1'	scale
000035D0	E5E2C3C8 D7404040			3326+V2_98	DC	A(RE98+16)	address of v2: 16-byte packed decimal
000035D8	00000010			3327+	DC	CL8' VSCHP'	instruction name
000035DC	00003600			3328+	DC	A(16)	result length
				3329+	DC	A(RE98)	address of expected result
				3330+*			
000035E0				3331+X98	DS	0F	
000035E0	E710 8F2C 0006		0000112C	3332+	VL	V1, V1FUDGE	fudge V1

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000035E6	E320 500C 0014		000035CC	3333+	LGF	R2, V2_98	get v2
000035EC	E722 0000 0006		00000000	3334+	VL	V2, 0(R2)	
000035F2	E730 5009 7000		000035C9	3335+	VLEB	V3, V3_98, 7	get v3 scale
000035F8	E612 3010 4074			3336+	VSCHP	V1, V2, V3, 4, 1	test instruction
000035FE	07FB			3337+	BR	R11	return
00003600				3338+RE98	DS	0F	expected 16 byte result
00003600				3339+	DROP	R5	
00003600	C1A00000 00000000		3340	DC	XL16'	C1A000000000000000B300000000000000	
00003608	B3000000 00000000						
00003610	00000000 00000000		3341	DC	XL16'	00000000000000000000000000000001D'	
00003618	00000000 0000001D						
				3342			
				3343 *	+900000000000000001		
				3344	VRR_B VSCHP, 4, 1, 2		
00003620				3345+	DS	0FD	
00003620		00003620		3346+	USING	*, R5	base for test data and test routine
00003620	00003640			3347+T99	DC	A(X99)	address of test routine
00003624	0063			3348+	DC	H' 99'	test number
00003626	00			3349+	DC	X' 00'	
00003627	04			3350+	DC	HL1' 4'	m4
00003628	01			3351+	DC	HL1' 1'	m5
00003629	02			3352+V3_99	DC	HL1' 2'	scale
0000362C	00003670			3353+V2_99	DC	A(RE99+16)	address of v2: 16-byte packed decimal
00003630	E5E2C3C8 D7404040			3354+	DC	CL8' VSCHP'	instruction name
00003638	00000010			3355+	DC	A(16)	result length
0000363C	00003660			3356+	DC	A(RE99)	address of expected result
				3357+*			
00003640				3358+X99	DS	0F	
00003640	E710 8F2C 0006		0000112C	3359+	VL	V1, V1FUDGE	fudge V1
00003646	E320 500C 0014		0000362C	3360+	LGF	R2, V2_99	get v2
0000364C	E722 0000 0006		00000000	3361+	VL	V2, 0(R2)	
00003652	E730 5009 7000		00003629	3362+	VLEB	V3, V3_99, 7	get v3 scale
00003658	E612 3010 4074			3363+	VSCHP	V1, V2, V3, 4, 1	test instruction
0000365E	07FB			3364+	BR	R11	return
00003660				3365+RE99	DS	0F	expected 16 byte result
00003660				3366+	DROP	R5	
00003660	4FC7D713 B49DA006		3367	DC	XL16'	4FC7D713B49DA0064140000000000000	
00003668	41400000 00000000						
00003670	00000000 00000009		3368	DC	XL16'	00000000000000009000000000000001C'	
00003678	00000000 0000001C						
				3369			
				3370 *	- 9223372036854775808		
				3371	VRR_B VSCHP, 4, 1, 2		
00003680				3372+	DS	0FD	
00003680		00003680		3373+	USING	*, R5	base for test data and test routine
00003680	000036A0			3374+T100	DC	A(X100)	address of test routine
00003684	0064			3375+	DC	H' 100'	test number
00003686	00			3376+	DC	X' 00'	
00003687	04			3377+	DC	HL1' 4'	m4
00003688	01			3378+	DC	HL1' 1'	m5
00003689	02			3379+V3_100	DC	HL1' 2'	scale
0000368C	000036D0			3380+V2_100	DC	A(RE100+16)	address of v2: 16-byte packed decimal
00003690	E5E2C3C8 D7404040			3381+	DC	CL8' VSCHP'	instruction name
00003698	00000010			3382+	DC	A(16)	result length
0000369C	000036C0			3383+	DC	A(RE100)	address of expected result
				3384+*			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000036A0				3385+X100	DS	0F	
000036A0	E710 8F2C 0006		0000112C	3386+	VL	V1, V1FUDGE	fudge V1
000036A6	E320 500C 0014		0000368C	3387+	LGF	R2, V2_100	get v2
000036AC	E722 0000 0006		00000000	3388+	VL	V2, 0(R2)	
000036B2	E730 5009 7000		00003689	3389+	VLEB	V3, V3_100, 7	get v3 scale
000036B8	E612 3010 4074			3390+	VSCHP	V1, V2, V3, 4, 1	test instruction
000036BE	07FB			3391+	BR	R11	return
000036C0				3392+RE100	DS	0F	expected 16 byte result
000036C0				3393+	DROP	R5	
000036C0	D2320000 00000000			3394	DC	XL16' D232000000000000C400000000000000'	
000036C8	C4000000 00000000						
000036D0	00000000 00009223			3395	DC	XL16' 00000000000009223372036854775808D'	
000036D8	37203685 4775808D						
				3396			
				3397 *		9223372036854775807	
				3398	VRR_B	VSCHP, 4, 1, 2	
000036E0				3399+	DS	0FD	
000036E0		000036E0		3400+	USING	*, R5	base for test data and test routine
000036E0	00003700			3401+T101	DC	A(X101)	address of test routine
000036E4	0065			3402+	DC	H' 101'	test number
000036E6	00			3403+	DC	X' 00'	
000036E7	04			3404+	DC	HL1' 4'	m4
000036E8	01			3405+	DC	HL1' 1'	m5
000036E9	02			3406+V3_101	DC	HL1' 2'	scale
000036EC	00003730			3407+V2_101	DC	A(RE101+16)	address of v2: 16-byte packed decimal
000036F0	E5E2C3C8 D7404040			3408+	DC	CL8' VSCHP'	instruction name
000036F8	00000010			3409+	DC	A(16)	result length
000036FC	00003720			3410+	DC	A(RE101)	address of expected result
				3411+*			
00003700				3412+X101	DS	0F	
00003700	E710 8F2C 0006		0000112C	3413+	VL	V1, V1FUDGE	fudge V1
00003706	E320 500C 0014		000036EC	3414+	LGF	R2, V2_101	get v2
0000370C	E722 0000 0006		00000000	3415+	VL	V2, 0(R2)	
00003712	E730 5009 7000		000036E9	3416+	VLEB	V3, V3_101, 7	get v3 scale
00003718	E612 3010 4074			3417+	VSCHP	V1, V2, V3, 4, 1	test instruction
0000371E	07FB			3418+	BR	R11	return
00003720				3419+RE101	DS	0F	expected 16 byte result
00003720				3420+	DROP	R5	
00003720	5231FFFF FFFFFFFF			3421	DC	XL16' 5231FFFFFFFFFFFF44FF9C0000000000'	
00003728	44FF9C00 00000000						
00003730	00000000 00009223			3422	DC	XL16' 00000000000009223372036854775807C'	
00003738	37203685 4775807C						
				3423			
				3424 *		18446744073709551615	
				3425	VRR_B	VSCHP, 4, 1, 2	
00003740				3426+	DS	0FD	
00003740		00003740		3427+	USING	*, R5	base for test data and test routine
00003740	00003760			3428+T102	DC	A(X102)	address of test routine
00003744	0066			3429+	DC	H' 102'	test number
00003746	00			3430+	DC	X' 00'	
00003747	04			3431+	DC	HL1' 4'	m4
00003748	01			3432+	DC	HL1' 1'	m5
00003749	02			3433+V3_102	DC	HL1' 2'	scale
0000374C	00003790			3434+V2_102	DC	A(RE102+16)	address of v2: 16-byte packed decimal
00003750	E5E2C3C8 D7404040			3435+	DC	CL8' VSCHP'	instruction name
00003758	00000010			3436+	DC	A(16)	result length

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0000375C	00003780			3437+ 3438+*	DC	A(RE102)	address of expected result
00003760				3439+X102	DS	OF	
00003760	E710 8F2C 0006		0000112C	3440+	VL	V1, V1FUDGE	fudge V1
00003766	E320 500C 0014		0000374C	3441+	LGF	R2, V2_102	get v2
0000376C	E722 0000 0006		00000000	3442+	VL	V2, 0(R2)	
00003772	E730 5009 7000		00003749	3443+	VLEB	V3, V3_102, 7	get v3 scale
00003778	E612 3010 4074			3444+	VSCHP	V1, V2, V3, 4, 1	test instruction
0000377E	07FB			3445+	BR	R11	return
00003780				3446+RE102	DS	OF	expected 16 byte result
00003780				3447+	DROP	R5	
00003780	5263FFFF FFFFFFFF			3448	DC	XL16' 5263FFFFFFFFFFFFFFFF44FF9C000000000000'	
00003788	44FF9C00 00000000						
00003790	00000000 00018446			3449	DC	XL16' 00000000000018446744073709551615C'	
00003798	74407370 9551615C						
				3450			
				3451 * 90090000000018446744073709551615			
				3452	VRR_B	VSCHP, 4, 1, 3	
000037A0				3453+	DS	OFD	
000037A0		000037A0		3454+	USING	*, R5	base for test data and test routine
000037A0	000037C0			3455+T103	DC	A(X103)	address of test routine
000037A4	0067			3456+	DC	H' 103'	test number
000037A6	00			3457+	DC	X' 00'	
000037A7	04			3458+	DC	HL1' 4'	m4
000037A8	01			3459+	DC	HL1' 1'	m5
000037A9	03			3460+V3_103	DC	HL1' 3'	scale
000037AC	000037F0			3461+V2_103	DC	A(RE103+16)	address of v2: 16-byte packed decimal
000037B0	E5E2C3C8 D7404040			3462+	DC	CL8' VSCHP'	instruction name
000037B8	00000010			3463+	DC	A(16)	result length
000037BC	000037E0			3464+	DC	A(RE103)	address of expected result
				3465+*			
000037C0				3466+X103	DS	OF	
000037C0	E710 8F2C 0006		0000112C	3467+	VL	V1, V1FUDGE	fudge V1
000037C6	E320 500C 0014		000037AC	3468+	LGF	R2, V2_103	get v2
000037CC	E722 0000 0006		00000000	3469+	VL	V2, 0(R2)	
000037D2	E730 5009 7000		000037A9	3470+	VLEB	V3, V3_103, 7	get v3 scale
000037D8	E612 3010 4074			3471+	VSCHP	V1, V2, V3, 4, 1	test instruction
000037DE	07FB			3472+	BR	R11	return
000037E0				3473+RE103	DS	OF	expected 16 byte result
000037E0				3474+	DROP	R5	
000037E0	5D1BC2D9 0FA81677			3475	DC	XL16' 5D1BC2D90FA816774F8E890963FFFFC2'	
000037E8	4F8E8909 63FFFFC2						
000037F0	90090000 00018446			3476	DC	XL16' 90090000000018446744073709551615C'	
000037F8	74407370 9551615C						
				3477			
				3478 * 9999999990018446744073709551615			
				3479	VRR_B	VSCHP, 4, 1, 3	
00003800				3480+	DS	OFD	
00003800		00003800		3481+	USING	*, R5	base for test data and test routine
00003800	00003820			3482+T104	DC	A(X104)	address of test routine
00003804	0068			3483+	DC	H' 104'	test number
00003806	00			3484+	DC	X' 00'	
00003807	04			3485+	DC	HL1' 4'	m4
00003808	01			3486+	DC	HL1' 1'	m5
00003809	03			3487+V3_104	DC	HL1' 3'	scale
0000380C	00003850			3488+V2_104	DC	A(RE104+16)	address of v2: 16-byte packed decimal

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00003810	E5E2C3C8 D7404040			3489+	DC	CL8' VSCHP'	instruction name
00003818	00000010			3490+	DC	A(16)	result length
0000381C	00003840			3491+	DC	A(RE104)	address of expected result
				3492+*			
00003820				3493+X104	DS	OF	
00003820	E710 8F2C 0006		0000112C	3494+	VL	V1, V1FUDGE	fudge V1
00003826	E320 500C 0014		0000380C	3495+	LGF	R2, V2_104	get v2
0000382C	E722 0000 0006		00000000	3496+	VL	V2, 0(R2)	
00003832	E730 5009 7000		00003809	3497+	VLEB	V3, V3_104, 7	get v3 scale
00003838	E612 3010 4074			3498+	VSCHP	V1, V2, V3, 4, 1	test instruction
0000383E	07FB			3499+	BR	R11	return
00003840				3500+RE104	DS	OF	expected 16 byte result
00003840				3501+	DROP	R5	
00003840	5D1ED09B EA546132			3502	DC	XL16' 5D1ED09BEA5461324F1798D1BB5FFFC2'	
00003848	4F1798D1 BB5FFFC2						
00003850	99999999 90018446			3503	DC	XL16' 9999999990018446744073709551615C'	
00003858	74407370 9551615C						
				3504			
				3505			
00003860	00000000			3506	DC	F' 0'	END OF TABLE
00003864	00000000			3507	DC	F' 0'	
				3508 *			
				3509 *		table of pointers to individual tests	
				3510 *			
00003868				3511 E6TESTS	DS	OF	
				3512	PTTABLE		
00003868				3513+TTABLE	DS	OF	
00003868	00001160			3514+	DC	A(T1)	TEST &CUR
0000386C	000011C0			3515+	DC	A(T2)	TEST &CUR
00003870	00001220			3516+	DC	A(T3)	TEST &CUR
00003874	00001280			3517+	DC	A(T4)	TEST &CUR
00003878	000012E0			3518+	DC	A(T5)	TEST &CUR
0000387C	00001340			3519+	DC	A(T6)	TEST &CUR
00003880	000013A0			3520+	DC	A(T7)	TEST &CUR
00003884	00001400			3521+	DC	A(T8)	TEST &CUR
00003888	00001460			3522+	DC	A(T9)	TEST &CUR
0000388C	000014C0			3523+	DC	A(T10)	TEST &CUR
00003890	00001520			3524+	DC	A(T11)	TEST &CUR
00003894	00001580			3525+	DC	A(T12)	TEST &CUR
00003898	000015E0			3526+	DC	A(T13)	TEST &CUR
0000389C	00001640			3527+	DC	A(T14)	TEST &CUR
000038A0	000016A0			3528+	DC	A(T15)	TEST &CUR
000038A4	00001700			3529+	DC	A(T16)	TEST &CUR
000038A8	00001760			3530+	DC	A(T17)	TEST &CUR
000038AC	000017C0			3531+	DC	A(T18)	TEST &CUR
000038B0	00001820			3532+	DC	A(T19)	TEST &CUR
000038B4	00001880			3533+	DC	A(T20)	TEST &CUR
000038B8	000018E0			3534+	DC	A(T21)	TEST &CUR
000038BC	00001940			3535+	DC	A(T22)	TEST &CUR
000038C0	000019A0			3536+	DC	A(T23)	TEST &CUR
000038C4	00001A00			3537+	DC	A(T24)	TEST &CUR
000038C8	00001A60			3538+	DC	A(T25)	TEST &CUR
000038CC	00001AC0			3539+	DC	A(T26)	TEST &CUR
000038D0	00001B20			3540+	DC	A(T27)	TEST &CUR
000038D4	00001B80			3541+	DC	A(T28)	TEST &CUR
000038D8	00001BE0			3542+	DC	A(T29)	TEST &CUR

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000038DC	00001C40			3543+	DC	A(T30)	TEST &CUR
000038E0	00001CA0			3544+	DC	A(T31)	TEST &CUR
000038E4	00001D00			3545+	DC	A(T32)	TEST &CUR
000038E8	00001D60			3546+	DC	A(T33)	TEST &CUR
000038EC	00001DC0			3547+	DC	A(T34)	TEST &CUR
000038F0	00001E20			3548+	DC	A(T35)	TEST &CUR
000038F4	00001E80			3549+	DC	A(T36)	TEST &CUR
000038F8	00001EE0			3550+	DC	A(T37)	TEST &CUR
000038FC	00001F40			3551+	DC	A(T38)	TEST &CUR
00003900	00001FA0			3552+	DC	A(T39)	TEST &CUR
00003904	00002000			3553+	DC	A(T40)	TEST &CUR
00003908	00002060			3554+	DC	A(T41)	TEST &CUR
0000390C	000020C0			3555+	DC	A(T42)	TEST &CUR
00003910	00002120			3556+	DC	A(T43)	TEST &CUR
00003914	00002180			3557+	DC	A(T44)	TEST &CUR
00003918	000021E0			3558+	DC	A(T45)	TEST &CUR
0000391C	00002240			3559+	DC	A(T46)	TEST &CUR
00003920	000022A0			3560+	DC	A(T47)	TEST &CUR
00003924	00002300			3561+	DC	A(T48)	TEST &CUR
00003928	00002360			3562+	DC	A(T49)	TEST &CUR
0000392C	000023C0			3563+	DC	A(T50)	TEST &CUR
00003930	00002420			3564+	DC	A(T51)	TEST &CUR
00003934	00002480			3565+	DC	A(T52)	TEST &CUR
00003938	000024E0			3566+	DC	A(T53)	TEST &CUR
0000393C	00002540			3567+	DC	A(T54)	TEST &CUR
00003940	000025A0			3568+	DC	A(T55)	TEST &CUR
00003944	00002600			3569+	DC	A(T56)	TEST &CUR
00003948	00002660			3570+	DC	A(T57)	TEST &CUR
0000394C	000026C0			3571+	DC	A(T58)	TEST &CUR
00003950	00002720			3572+	DC	A(T59)	TEST &CUR
00003954	00002780			3573+	DC	A(T60)	TEST &CUR
00003958	000027E0			3574+	DC	A(T61)	TEST &CUR
0000395C	00002840			3575+	DC	A(T62)	TEST &CUR
00003960	000028A0			3576+	DC	A(T63)	TEST &CUR
00003964	00002900			3577+	DC	A(T64)	TEST &CUR
00003968	00002960			3578+	DC	A(T65)	TEST &CUR
0000396C	000029C0			3579+	DC	A(T66)	TEST &CUR
00003970	00002A20			3580+	DC	A(T67)	TEST &CUR
00003974	00002A80			3581+	DC	A(T68)	TEST &CUR
00003978	00002AE0			3582+	DC	A(T69)	TEST &CUR
0000397C	00002B40			3583+	DC	A(T70)	TEST &CUR
00003980	00002BA0			3584+	DC	A(T71)	TEST &CUR
00003984	00002C00			3585+	DC	A(T72)	TEST &CUR
00003988	00002C60			3586+	DC	A(T73)	TEST &CUR
0000398C	00002CC0			3587+	DC	A(T74)	TEST &CUR
00003990	00002D20			3588+	DC	A(T75)	TEST &CUR
00003994	00002D80			3589+	DC	A(T76)	TEST &CUR
00003998	00002DE0			3590+	DC	A(T77)	TEST &CUR
0000399C	00002E40			3591+	DC	A(T78)	TEST &CUR
000039A0	00002EA0			3592+	DC	A(T79)	TEST &CUR
000039A4	00002F00			3593+	DC	A(T80)	TEST &CUR
000039A8	00002F60			3594+	DC	A(T81)	TEST &CUR
000039AC	00002FC0			3595+	DC	A(T82)	TEST &CUR
000039B0	00003020			3596+	DC	A(T83)	TEST &CUR
000039B4	00003080			3597+	DC	A(T84)	TEST &CUR
000039B8	000030E0			3598+	DC	A(T85)	TEST &CUR



LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				3625	*****
				3626	*            Register equates
				3627	*****
		00000000	00000001	3629 R0	EQU 0
		00000001	00000001	3630 R1	EQU 1
		00000002	00000001	3631 R2	EQU 2
		00000003	00000001	3632 R3	EQU 3
		00000004	00000001	3633 R4	EQU 4
		00000005	00000001	3634 R5	EQU 5
		00000006	00000001	3635 R6	EQU 6
		00000007	00000001	3636 R7	EQU 7
		00000008	00000001	3637 R8	EQU 8
		00000009	00000001	3638 R9	EQU 9
		0000000A	00000001	3639 R10	EQU 10
		0000000B	00000001	3640 R11	EQU 11
		0000000C	00000001	3641 R12	EQU 12
		0000000D	00000001	3642 R13	EQU 13
		0000000E	00000001	3643 R14	EQU 14
		0000000F	00000001	3644 R15	EQU 15
				3646	*****
				3647	*            Register equates
				3648	*****
		00000000	00000001	3650 FPR0	EQU 0
		00000001	00000001	3651 FPR1	EQU 1
		00000002	00000001	3652 FPR2	EQU 2
		00000003	00000001	3653 FPR3	EQU 3
		00000004	00000001	3654 FPR4	EQU 4
		00000005	00000001	3655 FPR5	EQU 5
		00000006	00000001	3656 FPR6	EQU 6
		00000007	00000001	3657 FPR7	EQU 7
		00000008	00000001	3658 FPR8	EQU 8
		00000009	00000001	3659 FPR9	EQU 9
		0000000A	00000001	3660 FPR10	EQU 10
		0000000B	00000001	3661 FPR11	EQU 11
		0000000C	00000001	3662 FPR12	EQU 12
		0000000D	00000001	3663 FPR13	EQU 13
		0000000E	00000001	3664 FPR14	EQU 14
		0000000F	00000001	3665 FPR15	EQU 15
				3667	*****
				3668	*            Register equates
				3669	*****
		00000000	00000001	3671 V0	EQU 0
		00000001	00000001	3672 V1	EQU 1
		00000002	00000001	3673 V2	EQU 2
		00000003	00000001	3674 V3	EQU 3
		00000004	00000001	3675 V4	EQU 4







SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES				
					3447	3454	3474	3481	3501
R6	U	00000006	1	3635					
R7	U	00000007	1	3636					
R8	U	00000008	1	3637	157	161	162	163	165
R9	U	00000009	1	3638	158	165	166	168	
RE1	F	000011A0	4	714	702	705			
RE10	F	00001500	4	956	944	947			
RE100	F	000036C0	4	3392	3380	3383			
RE101	F	00003720	4	3419	3407	3410			
RE102	F	00003780	4	3446	3434	3437			
RE103	F	000037E0	4	3473	3461	3464			
RE104	F	00003840	4	3500	3488	3491			
RE11	F	00001560	4	982	970	973			
RE12	F	000015C0	4	1008	996	999			
RE13	F	00001620	4	1035	1023	1026			
RE14	F	00001680	4	1062	1050	1053			
RE15	F	000016E0	4	1089	1077	1080			
RE16	F	00001740	4	1116	1104	1107			
RE17	F	000017A0	4	1146	1134	1137			
RE18	F	00001800	4	1172	1160	1163			
RE19	F	00001860	4	1198	1186	1189			
RE2	F	00001200	4	740	728	731			
RE20	F	000018C0	4	1224	1212	1215			
RE21	F	00001920	4	1251	1239	1242			
RE22	F	00001980	4	1278	1266	1269			
RE23	F	000019E0	4	1305	1293	1296			
RE24	F	00001A40	4	1332	1320	1323			
RE25	F	00001AA0	4	1365	1353	1356			
RE26	F	00001B00	4	1391	1379	1382			
RE27	F	00001B60	4	1417	1405	1408			
RE28	F	00001BC0	4	1443	1431	1434			
RE29	F	00001C20	4	1470	1458	1461			
RE3	F	00001260	4	766	754	757			
RE30	F	00001C80	4	1497	1485	1488			
RE31	F	00001CE0	4	1524	1512	1515			
RE32	F	00001D40	4	1551	1539	1542			
RE33	F	00001DA0	4	1581	1569	1572			
RE34	F	00001E00	4	1607	1595	1598			
RE35	F	00001E60	4	1633	1621	1624			
RE36	F	00001EC0	4	1659	1647	1650			
RE37	F	00001F20	4	1686	1674	1677			
RE38	F	00001F80	4	1713	1701	1704			
RE39	F	00001FE0	4	1740	1728	1731			
RE4	F	000012C0	4	792	780	783			
RE40	F	00002040	4	1767	1755	1758			
RE41	F	000020A0	4	1797	1785	1788			
RE42	F	00002100	4	1823	1811	1814			
RE43	F	00002160	4	1849	1837	1840			
RE44	F	000021C0	4	1875	1863	1866			
RE45	F	00002220	4	1902	1890	1893			
RE46	F	00002280	4	1929	1917	1920			
RE47	F	000022E0	4	1956	1944	1947			
RE48	F	00002340	4	1983	1971	1974			
RE49	F	000023A0	4	2016	2004	2007			
RE5	F	00001320	4	819	807	810			
RE50	F	00002400	4	2042	2030	2033			

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
RE51	F	00002460	4	2068	2056 2059
RE52	F	000024C0	4	2094	2082 2085
RE53	F	00002520	4	2121	2109 2112
RE54	F	00002580	4	2148	2136 2139
RE55	F	000025E0	4	2175	2163 2166
RE56	F	00002640	4	2202	2190 2193
RE57	F	000026A0	4	2232	2220 2223
RE58	F	00002700	4	2258	2246 2249
RE59	F	00002760	4	2284	2272 2275
RE6	F	00001380	4	846	834 837
RE60	F	000027C0	4	2310	2298 2301
RE61	F	00002820	4	2337	2325 2328
RE62	F	00002880	4	2364	2352 2355
RE63	F	000028E0	4	2391	2379 2382
RE64	F	00002940	4	2418	2406 2409
RE65	F	000029A0	4	2445	2433 2436
RE66	F	00002A00	4	2472	2460 2463
RE67	F	00002A60	4	2501	2489 2492
RE68	F	00002AC0	4	2527	2515 2518
RE69	F	00002B20	4	2553	2541 2544
RE7	F	000013E0	4	873	861 864
RE70	F	00002B80	4	2579	2567 2570
RE71	F	00002BE0	4	2606	2594 2597
RE72	F	00002C40	4	2633	2621 2624
RE73	F	00002CA0	4	2660	2648 2651
RE74	F	00002D00	4	2687	2675 2678
RE75	F	00002D60	4	2714	2702 2705
RE76	F	00002DC0	4	2741	2729 2732
RE77	F	00002E20	4	2775	2763 2766
RE78	F	00002E80	4	2801	2789 2792
RE79	F	00002EE0	4	2827	2815 2818
RE8	F	00001440	4	900	888 891
RE80	F	00002F40	4	2853	2841 2844
RE81	F	00002FA0	4	2880	2868 2871
RE82	F	00003000	4	2907	2895 2898
RE83	F	00003060	4	2934	2922 2925
RE84	F	000030C0	4	2961	2949 2952
RE85	F	00003120	4	2991	2979 2982
RE86	F	00003180	4	3017	3005 3008
RE87	F	000031E0	4	3043	3031 3034
RE88	F	00003240	4	3069	3057 3060
RE89	F	000032A0	4	3096	3084 3087
RE9	F	000014A0	4	930	918 921
RE90	F	00003300	4	3123	3111 3114
RE91	F	00003360	4	3150	3138 3141
RE92	F	000033C0	4	3177	3165 3168
RE93	F	00003420	4	3204	3192 3195
RE94	F	00003480	4	3231	3219 3222
RE95	F	000034E0	4	3260	3248 3251
RE96	F	00003540	4	3286	3274 3277
RE97	F	000035A0	4	3312	3300 3303
RE98	F	00003600	4	3338	3326 3329
RE99	F	00003660	4	3365	3353 3356
READDR	A	0000001C	4	594	233
REG2LOW	U	000000DD	1	508	
REG2PATT	U	AABBCCDD	1	507	

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES	
RELEN	A	00000018	4	593		
RPTDWSAV	D	00000550	8	428	417	419
RPTERROR	I	00000524	4	412	336	384
RPTSAVE	F	00000544	4	425	412	422
RPTSVR5	F	00000548	4	426	413	421
SCALE	U	00000009	1	590	327	376
SKL0001	U	0000006B	1	187	203	
SKT0001	C	0000022A	26	184	187	204
SVOLDPSW	U	00000140	0	122		
T1	A	00001160	4	696	3514	
T10	A	000014C0	4	938	3523	
T100	A	00003680	4	3374	3613	
T101	A	000036E0	4	3401	3614	
T102	A	00003740	4	3428	3615	
T103	A	000037A0	4	3455	3616	
T104	A	00003800	4	3482	3617	
T11	A	00001520	4	964	3524	
T12	A	00001580	4	990	3525	
T13	A	000015E0	4	1017	3526	
T14	A	00001640	4	1044	3527	
T15	A	000016A0	4	1071	3528	
T16	A	00001700	4	1098	3529	
T17	A	00001760	4	1128	3530	
T18	A	000017C0	4	1154	3531	
T19	A	00001820	4	1180	3532	
T2	A	000011C0	4	722	3515	
T20	A	00001880	4	1206	3533	
T21	A	000018E0	4	1233	3534	
T22	A	00001940	4	1260	3535	
T23	A	000019A0	4	1287	3536	
T24	A	00001A00	4	1314	3537	
T25	A	00001A60	4	1347	3538	
T26	A	00001AC0	4	1373	3539	
T27	A	00001B20	4	1399	3540	
T28	A	00001B80	4	1425	3541	
T29	A	00001BE0	4	1452	3542	
T3	A	00001220	4	748	3516	
T30	A	00001C40	4	1479	3543	
T31	A	00001CA0	4	1506	3544	
T32	A	00001D00	4	1533	3545	
T33	A	00001D60	4	1563	3546	
T34	A	00001DC0	4	1589	3547	
T35	A	00001E20	4	1615	3548	
T36	A	00001E80	4	1641	3549	
T37	A	00001EE0	4	1668	3550	
T38	A	00001F40	4	1695	3551	
T39	A	00001FA0	4	1722	3552	
T4	A	00001280	4	774	3517	
T40	A	00002000	4	1749	3553	
T41	A	00002060	4	1779	3554	
T42	A	000020C0	4	1805	3555	
T43	A	00002120	4	1831	3556	
T44	A	00002180	4	1857	3557	
T45	A	000021E0	4	1884	3558	
T46	A	00002240	4	1911	3559	
T47	A	000022A0	4	1938	3560	

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
T48	A	00002300	4	1965	3561
T49	A	00002360	4	1998	3562
T5	A	000012E0	4	801	3518
T50	A	000023C0	4	2024	3563
T51	A	00002420	4	2050	3564
T52	A	00002480	4	2076	3565
T53	A	000024E0	4	2103	3566
T54	A	00002540	4	2130	3567
T55	A	000025A0	4	2157	3568
T56	A	00002600	4	2184	3569
T57	A	00002660	4	2214	3570
T58	A	000026C0	4	2240	3571
T59	A	00002720	4	2266	3572
T6	A	00001340	4	828	3519
T60	A	00002780	4	2292	3573
T61	A	000027E0	4	2319	3574
T62	A	00002840	4	2346	3575
T63	A	000028A0	4	2373	3576
T64	A	00002900	4	2400	3577
T65	A	00002960	4	2427	3578
T66	A	000029C0	4	2454	3579
T67	A	00002A20	4	2483	3580
T68	A	00002A80	4	2509	3581
T69	A	00002AE0	4	2535	3582
T7	A	000013A0	4	855	3520
T70	A	00002B40	4	2561	3583
T71	A	00002BA0	4	2588	3584
T72	A	00002C00	4	2615	3585
T73	A	00002C60	4	2642	3586
T74	A	00002CC0	4	2669	3587
T75	A	00002D20	4	2696	3588
T76	A	00002D80	4	2723	3589
T77	A	00002DE0	4	2757	3590
T78	A	00002E40	4	2783	3591
T79	A	00002EA0	4	2809	3592
T8	A	00001400	4	882	3521
T80	A	00002F00	4	2835	3593
T81	A	00002F60	4	2862	3594
T82	A	00002FC0	4	2889	3595
T83	A	00003020	4	2916	3596
T84	A	00003080	4	2943	3597
T85	A	000030E0	4	2973	3598
T86	A	00003140	4	2999	3599
T87	A	000031A0	4	3025	3600
T88	A	00003200	4	3051	3601
T89	A	00003260	4	3078	3602
T9	A	00001460	4	912	3522
T90	A	000032C0	4	3105	3603
T91	A	00003320	4	3132	3604
T92	A	00003380	4	3159	3605
T93	A	000033E0	4	3186	3606
T94	A	00003440	4	3213	3607
T95	A	000034A0	4	3242	3608
T96	A	00003500	4	3268	3609
T97	A	00003560	4	3294	3610
T98	A	000035C0	4	3320	3611

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES												
T99	A	00003620	4	3347	3612												
TESTING	F	00001004	4	519	224												
TNUM	H	00000004	2	586	223	304	353										
TSUB	A	00000000	4	585	226												
TTABLE	F	00003868	4	3513													
V0	U	00000000	1	3671													
V1	U	00000001	1	3672	229	298	708	712	734	738	760	764	786	790	813	817	840
					844	867	871	894	898	924	928	950	954	976	980	1002	1006
					1029	1033	1056	1060	1083	1087	1110	1114	1140	1144	1166	1170	1192
					1196	1218	1222	1245	1249	1272	1276	1299	1303	1326	1330	1359	1363
					1385	1389	1411	1415	1437	1441	1464	1468	1491	1495	1518	1522	1545
					1549	1575	1579	1601	1605	1627	1631	1653	1657	1680	1684	1707	1711
					1734	1738	1761	1765	1791	1795	1817	1821	1843	1847	1869	1873	1896
					1900	1923	1927	1950	1954	1977	1981	2010	2014	2036	2040	2062	2066
					2088	2092	2115	2119	2142	2146	2169	2173	2196	2200	2226	2230	2252
					2256	2278	2282	2304	2308	2331	2335	2358	2362	2385	2389	2412	2416
					2439	2443	2466	2470	2495	2499	2521	2525	2547	2551	2573	2577	2600
					2604	2627	2631	2654	2658	2681	2685	2708	2712	2735	2739	2769	2773
					2795	2799	2821	2825	2847	2851	2874	2878	2901	2905	2928	2932	2955
					2959	2985	2989	3011	3015	3037	3041	3063	3067	3090	3094	3117	3121
					3144	3148	3171	3175	3198	3202	3225	3229	3254	3258	3280	3284	3306
					3310	3332	3336	3359	3363	3386	3390	3413	3417	3440	3444	3467	3471
					3494	3498											
V10	U	0000000A	1	3681	257	260											
V11	U	0000000B	1	3682	256	257											
V12	U	0000000C	1	3683													
V13	U	0000000D	1	3684													
V14	U	0000000E	1	3685													
V15	U	0000000F	1	3686													
V16	U	00000010	1	3687													
V17	U	00000011	1	3688													
V18	U	00000012	1	3689													
V19	U	00000013	1	3690													
V1FUDGE	X	0000112C	16	576	708	734	760	786	813	840	867	894	924	950	976	1002	1029
					1056	1083	1110	1140	1166	1192	1218	1245	1272	1299	1326	1359	1385
					1411	1437	1464	1491	1518	1545	1575	1601	1627	1653	1680	1707	1734
					1761	1791	1817	1843	1869	1896	1923	1950	1977	2010	2036	2062	2088
					2115	2142	2169	2196	2226	2252	2278	2304	2331	2358	2385	2412	2439
					2466	2495	2521	2547	2573	2600	2627	2654	2681	2708	2735	2769	2795
					2821	2847	2874	2901	2928	2955	2985	3011	3037	3063	3090	3117	3144
					3171	3198	3225	3254	3280	3306	3332	3359	3386	3413	3440	3467	3494
V1INPUT	X	0000113C	16	577													
V1OUTPUT	X	0000110C	16	574	229	234											
V2	U	00000002	1	3673	256	710	712	736	738	762	764	788	790	815	817	842	844
					869	871	896	898	926	928	952	954	978	980	1004	1006	1031
					1033	1058	1060	1085	1087	1112	1114	1142	1144	1168	1170	1194	1196
					1220	1222	1247	1249	1274	1276	1301	1303	1328	1330	1361	1363	1387
					1389	1413	1415	1439	1441	1466	1468	1493	1495	1520	1522	1547	1549
					1577	1579	1603	1605	1629	1631	1655	1657	1682	1684	1709	1711	1736
					1738	1763	1765	1793	1795	1819	1821	1845	1847	1871	1873	1898	1900
					1925	1927	1952	1954	1979	1981	2012	2014	2038	2040	2064	2066	2090
					2092	2117	2119	2144	2146	2171	2173	2198	2200	2228	2230	2254	2256
					2280	2282	2306	2308	2333	2335	2360	2362	2387	2389	2414	2416	2441
					2443	2468	2470	2497	2499	2523	2525	2549	2551	2575	2577	2602	2604
					2629	2631	2656	2658	2683	2685	2710	2712	2737	2739	2771	2773	2797
					2799	2823	2825	2849	2851	2876	2878	2903	2905	2930	2932	2957	2959



SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
V2_42	A	000020CC	4	1811	1818
V2_43	A	0000212C	4	1837	1844
V2_44	A	0000218C	4	1863	1870
V2_45	A	000021EC	4	1890	1897
V2_46	A	0000224C	4	1917	1924
V2_47	A	000022AC	4	1944	1951
V2_48	A	0000230C	4	1971	1978
V2_49	A	0000236C	4	2004	2011
V2_5	A	000012EC	4	807	814
V2_50	A	000023CC	4	2030	2037
V2_51	A	0000242C	4	2056	2063
V2_52	A	0000248C	4	2082	2089
V2_53	A	000024EC	4	2109	2116
V2_54	A	0000254C	4	2136	2143
V2_55	A	000025AC	4	2163	2170
V2_56	A	0000260C	4	2190	2197
V2_57	A	0000266C	4	2220	2227
V2_58	A	000026CC	4	2246	2253
V2_59	A	0000272C	4	2272	2279
V2_6	A	0000134C	4	834	841
V2_60	A	0000278C	4	2298	2305
V2_61	A	000027EC	4	2325	2332
V2_62	A	0000284C	4	2352	2359
V2_63	A	000028AC	4	2379	2386
V2_64	A	0000290C	4	2406	2413
V2_65	A	0000296C	4	2433	2440
V2_66	A	000029CC	4	2460	2467
V2_67	A	00002A2C	4	2489	2496
V2_68	A	00002A8C	4	2515	2522
V2_69	A	00002AEC	4	2541	2548
V2_7	A	000013AC	4	861	868
V2_70	A	00002B4C	4	2567	2574
V2_71	A	00002BAC	4	2594	2601
V2_72	A	00002C0C	4	2621	2628
V2_73	A	00002C6C	4	2648	2655
V2_74	A	00002CCC	4	2675	2682
V2_75	A	00002D2C	4	2702	2709
V2_76	A	00002D8C	4	2729	2736
V2_77	A	00002DEC	4	2763	2770
V2_78	A	00002E4C	4	2789	2796
V2_79	A	00002EAC	4	2815	2822
V2_8	A	0000140C	4	888	895
V2_80	A	00002F0C	4	2841	2848
V2_81	A	00002F6C	4	2868	2875
V2_82	A	00002FCC	4	2895	2902
V2_83	A	0000302C	4	2922	2929
V2_84	A	0000308C	4	2949	2956
V2_85	A	000030EC	4	2979	2986
V2_86	A	0000314C	4	3005	3012
V2_87	A	000031AC	4	3031	3038
V2_88	A	0000320C	4	3057	3064
V2_89	A	0000326C	4	3084	3091
V2_9	A	0000146C	4	918	925
V2_90	A	000032CC	4	3111	3118
V2_91	A	0000332C	4	3138	3145
V2_92	A	0000338C	4	3165	3172



SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
V3_32	U	00001D09	1	1538	1548
V3_33	U	00001D69	1	1568	1578
V3_34	U	00001DC9	1	1594	1604
V3_35	U	00001E29	1	1620	1630
V3_36	U	00001E89	1	1646	1656
V3_37	U	00001EE9	1	1673	1683
V3_38	U	00001F49	1	1700	1710
V3_39	U	00001FA9	1	1727	1737
V3_4	U	00001289	1	779	789
V3_40	U	00002009	1	1754	1764
V3_41	U	00002069	1	1784	1794
V3_42	U	000020C9	1	1810	1820
V3_43	U	00002129	1	1836	1846
V3_44	U	00002189	1	1862	1872
V3_45	U	000021E9	1	1889	1899
V3_46	U	00002249	1	1916	1926
V3_47	U	000022A9	1	1943	1953
V3_48	U	00002309	1	1970	1980
V3_49	U	00002369	1	2003	2013
V3_5	U	000012E9	1	806	816
V3_50	U	000023C9	1	2029	2039
V3_51	U	00002429	1	2055	2065
V3_52	U	00002489	1	2081	2091
V3_53	U	000024E9	1	2108	2118
V3_54	U	00002549	1	2135	2145
V3_55	U	000025A9	1	2162	2172
V3_56	U	00002609	1	2189	2199
V3_57	U	00002669	1	2219	2229
V3_58	U	000026C9	1	2245	2255
V3_59	U	00002729	1	2271	2281
V3_6	U	00001349	1	833	843
V3_60	U	00002789	1	2297	2307
V3_61	U	000027E9	1	2324	2334
V3_62	U	00002849	1	2351	2361
V3_63	U	000028A9	1	2378	2388
V3_64	U	00002909	1	2405	2415
V3_65	U	00002969	1	2432	2442
V3_66	U	000029C9	1	2459	2469
V3_67	U	00002A29	1	2488	2498
V3_68	U	00002A89	1	2514	2524
V3_69	U	00002AE9	1	2540	2550
V3_7	U	000013A9	1	860	870
V3_70	U	00002B49	1	2566	2576
V3_71	U	00002BA9	1	2593	2603
V3_72	U	00002C09	1	2620	2630
V3_73	U	00002C69	1	2647	2657
V3_74	U	00002CC9	1	2674	2684
V3_75	U	00002D29	1	2701	2711
V3_76	U	00002D89	1	2728	2738
V3_77	U	00002DE9	1	2762	2772
V3_78	U	00002E49	1	2788	2798
V3_79	U	00002EA9	1	2814	2824
V3_8	U	00001409	1	887	897
V3_80	U	00002F09	1	2840	2850
V3_81	U	00002F69	1	2867	2877
V3_82	U	00002FC9	1	2894	2904

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES				
V3_83	U	00003029	1	2921	2931				
V3_84	U	00003089	1	2948	2958				
V3_85	U	000030E9	1	2978	2988				
V3_86	U	00003149	1	3004	3014				
V3_87	U	000031A9	1	3030	3040				
V3_88	U	00003209	1	3056	3066				
V3_89	U	00003269	1	3083	3093				
V3_9	U	00001469	1	917	927				
V3_90	U	000032C9	1	3110	3120				
V3_91	U	00003329	1	3137	3147				
V3_92	U	00003389	1	3164	3174				
V3_93	U	000033E9	1	3191	3201				
V3_94	U	00003449	1	3218	3228				
V3_95	U	000034A9	1	3247	3257				
V3_96	U	00003509	1	3273	3283				
V3_97	U	00003569	1	3299	3309				
V3_98	U	000035C9	1	3325	3335				
V3_99	U	00003629	1	3352	3362				
V4	U	00000004	1	3675	263	282	288	294	
V5	U	00000005	1	3676					
V6	U	00000006	1	3677	264	295			
V7	U	00000007	1	3678					
V8	U	00000008	1	3679					
V9	U	00000009	1	3680					
X0001	U	000002C8	1	193	181	194			
X1	F	00001180	4	707	696				
X10	F	000014E0	4	949	938				
X100	F	000036A0	4	3385	3374				
X101	F	00003700	4	3412	3401				
X102	F	00003760	4	3439	3428				
X103	F	000037C0	4	3466	3455				
X104	F	00003820	4	3493	3482				
X11	F	00001540	4	975	964				
X12	F	000015A0	4	1001	990				
X13	F	00001600	4	1028	1017				
X14	F	00001660	4	1055	1044				
X15	F	000016C0	4	1082	1071				
X16	F	00001720	4	1109	1098				
X17	F	00001780	4	1139	1128				
X18	F	000017E0	4	1165	1154				
X19	F	00001840	4	1191	1180				
X2	F	000011E0	4	733	722				
X20	F	000018A0	4	1217	1206				
X21	F	00001900	4	1244	1233				
X22	F	00001960	4	1271	1260				
X23	F	000019C0	4	1298	1287				
X24	F	00001A20	4	1325	1314				
X25	F	00001A80	4	1358	1347				
X26	F	00001AE0	4	1384	1373				
X27	F	00001B40	4	1410	1399				
X28	F	00001BA0	4	1436	1425				
X29	F	00001C00	4	1463	1452				
X3	F	00001240	4	759	748				
X30	F	00001C60	4	1490	1479				
X31	F	00001CC0	4	1517	1506				
X32	F	00001D20	4	1544	1533				

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
X33	F	00001D80	4	1574	1563
X34	F	00001DE0	4	1600	1589
X35	F	00001E40	4	1626	1615
X36	F	00001EA0	4	1652	1641
X37	F	00001F00	4	1679	1668
X38	F	00001F60	4	1706	1695
X39	F	00001FC0	4	1733	1722
X4	F	000012A0	4	785	774
X40	F	00002020	4	1760	1749
X41	F	00002080	4	1790	1779
X42	F	000020E0	4	1816	1805
X43	F	00002140	4	1842	1831
X44	F	000021A0	4	1868	1857
X45	F	00002200	4	1895	1884
X46	F	00002260	4	1922	1911
X47	F	000022C0	4	1949	1938
X48	F	00002320	4	1976	1965
X49	F	00002380	4	2009	1998
X5	F	00001300	4	812	801
X50	F	000023E0	4	2035	2024
X51	F	00002440	4	2061	2050
X52	F	000024A0	4	2087	2076
X53	F	00002500	4	2114	2103
X54	F	00002560	4	2141	2130
X55	F	000025C0	4	2168	2157
X56	F	00002620	4	2195	2184
X57	F	00002680	4	2225	2214
X58	F	000026E0	4	2251	2240
X59	F	00002740	4	2277	2266
X6	F	00001360	4	839	828
X60	F	000027A0	4	2303	2292
X61	F	00002800	4	2330	2319
X62	F	00002860	4	2357	2346
X63	F	000028C0	4	2384	2373
X64	F	00002920	4	2411	2400
X65	F	00002980	4	2438	2427
X66	F	000029E0	4	2465	2454
X67	F	00002A40	4	2494	2483
X68	F	00002AA0	4	2520	2509
X69	F	00002B00	4	2546	2535
X7	F	000013C0	4	866	855
X70	F	00002B60	4	2572	2561
X71	F	00002BC0	4	2599	2588
X72	F	00002C20	4	2626	2615
X73	F	00002C80	4	2653	2642
X74	F	00002CE0	4	2680	2669
X75	F	00002D40	4	2707	2696
X76	F	00002DA0	4	2734	2723
X77	F	00002E00	4	2768	2757
X78	F	00002E60	4	2794	2783
X79	F	00002EC0	4	2820	2809
X8	F	00001420	4	893	882
X80	F	00002F20	4	2846	2835
X81	F	00002F80	4	2873	2862
X82	F	00002FE0	4	2900	2889
X83	F	00003040	4	2927	2916

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
X84	F	000030A0	4	2954	2943
X85	F	00003100	4	2984	2973
X86	F	00003160	4	3010	2999
X87	F	000031C0	4	3036	3025
X88	F	00003220	4	3062	3051
X89	F	00003280	4	3089	3078
X9	F	00001480	4	923	912
X90	F	000032E0	4	3116	3105
X91	F	00003340	4	3143	3132
X92	F	000033A0	4	3170	3159
X93	F	00003400	4	3197	3186
X94	F	00003460	4	3224	3213
X95	F	000034C0	4	3253	3242
X96	F	00003520	4	3279	3268
X97	F	00003580	4	3305	3294
X98	F	000035E0	4	3331	3320
X99	F	00003640	4	3358	3347
XC0001	U	000002F0	1	207	199
XC001	U	000003B4	1	297	283
XCEXT	F	000003A4	4	292	276
XCHECK	U	0000032E	1	248	231
XCLONG	F	00000394	4	286	274
XCPLINE	C	00001060	13	546	557
XCPLNG	U	0000005F	1	557	334
XCPM4	C	000010A0	2	551	317
XCPM5	C	000010AC	2	553	324
XCPNAME	C	0000108F	8	549	310
XCPSCALE	C	000010BB	3	555	331
XCPTNUM	C	0000106D	3	547	308
XCR15	F	00000478	8	344	333
XCRESULT	X	00000458	16	342	282
XCSHORT	F	00000384	4	280	272
XCV1	X	00000468	16	343	298
ZVE6TST	J	00000000	14872	119	122
=A(E6TESTS)	A	00000650	4	491	214
=AL2(L' MSGMSG)	R	0000066A	2	498	440
=F' 0'	F	00000654	4	492	263
=F' 1'	F	00000664	4	496	390
=F' 128'	F	0000064C	4	490	198
=F' 2'	F	00000658	4	493	271
=F' 3'	F	0000065C	4	494	273
=F' 4'	F	00000660	4	495	275
=H' 0'	H	00000668	2	497	435



DESC	SYMBOL	SIZE	POS	ADDR
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**Entry: 0**

<b>Image</b>	<b>IMAGE</b>	<b>14872</b>	<b>0000- 3A17</b>	<b>0000- 3A17</b>
<b>Regi on</b>		<b>14872</b>	<b>0000- 3A17</b>	<b>0000- 3A17</b>
<b>CSECT</b>	<b>ZVE6TST</b>	<b>14872</b>	<b>0000- 3A17</b>	<b>0000- 3A17</b>

STMT	FILE NAME
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1	/home/tn529/sharedvfp/tests/zvector-e6-17-VSCHP.asm
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**\*\* NO ERRORS FOUND \*\***